

FACULTY OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

19th September 2025

Capt. Dr. K. P. Sridhar

Professor

Department of Electronics and Communication Engineering

Karpagam Academy of Higher Education

Coimbatore-641021

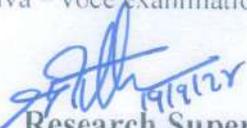
Ph.D. Public Viva - Voce Examination

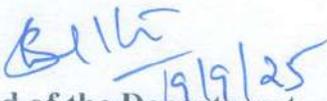
The duly constituted board of examiners' has approved the Ph.D. thesis submitted by the research scholar **Mr. G R Mahendra Babu (19JLDRPECE005)**, for the award of degree of Doctor of Philosophy (Ph.D.) in Electronics and Communication Engineering. In this regard, I am pleased to inform you that the Ph.D. public viva - voce examination of the research scholar **Mr. G R Mahendra Babu** is scheduled to be conducted on **October 10, 2025 (Friday)**, by **10.30 a.m.** at **FoE Seminar Hall (above HDFC Bank), Karpagam Academy of Higher Education, Coimbatore - 641021.**

Title of Ph.D. Thesis : ENERGY EFFICIENT-ULTRA LOW POWER ERROR TOLERANT ADDERS FOR NEURAL NETWORK HARDWARE ACCELERATORS

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A copy of the Ph.D. thesis is placed in the university library for the reference. All the faculty members, research scholars and students are cordially invited to participate in the above Ph.D. public viva - voce examination.


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