

## FACULTY OF ENGINEERING

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

10<sup>th</sup> December 2025

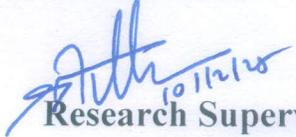
Capt. Dr. K. P. Sridhar  
Professor,  
Department of Electronics and Communication Engineering  
Karpagam Academy of Higher Education  
Coimbatore-641021

### Ph.D. Public Viva - Voce Examination

The duly constituted board of examiners' has approved the Ph.D. thesis submitted by the research scholar **Mr. Surenther I (Reg. No. 21Jan/ECE/Ph.D.PT/004)** for the award of degree of Doctor of Philosophy (Ph.D.) in Electronics and Communication Engineering. In this regard, I am pleased to inform you that the Ph.D. public viva - voce examination of the research scholar Mr. Surenther I is scheduled to be conducted on **December 23, 2025 (Tuesday), by 10.30 a.m. at FoE Seminar Hall (above HDFC Bank), Karpagam Academy of Higher Education, Coimbatore – 641021.**

**Title of Ph.D. Thesis :** OPTIMIZED ENERGY EFFICIENT DATA TRANSMISSION IN WIRELESS SENSOR NETWORK USING HEURISTIC GROUPING MODELS  
**Name and Designation of External Examiner :** Dr. Sabu M. K  
Professor,  
Department of Computer Applications,  
Cochin University of Science and Technology,  
Cochin – 682 022, Kerala, India.

A copy of the Ph.D. thesis is placed in the university library for the reference. All the faculty members, research scholars and students are cordially invited to participate in the above Ph.D. public viva – voce examination.



Research Supervisor

**Capt. Dr. K.P. SRIDHAR**  
Professor / Dept of ECE  
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Head of the Department

**Dr.S.MALATHY M.E, Ph.D**  
Professor & Head,  
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Copy To :

1. The Vice Chancellor
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