

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### REPORT

<b>Program Name</b>	Workshop
<b>Program Title</b>	Hands on Workshop on Advanced Digital Logic System Using Cadence EDA Tools
<b>Academic Year</b>	2015-2016
<b>Date &amp; Time</b>	<b>27-02-2016</b> <b>(9.00 am to 5.00 pm)</b>
<b>Beneficiary Students</b>	UG,PG, Research Scholars & Faculty
<b>Resource Person Details</b>	Mr.C.Pandiarajan & Mr.G.R.Mahendra Babu, Assistant Professor, Dept of ECE , FoE(CB), Karpagam Academy of Higher Education
<b>Organized by</b>	Mr.C.Pandiarajan & Mr.G.R.Mahendra Babu,
<b>No of Participants</b>	<b>60</b>

Department of ECE organized Workshop on “Hands on Workshop on Advanced Digital Logic System Using Cadence EDA Tools” on 27<sup>th</sup> February 2016. 60 candidates participated including UG, PG & faculty members from in and around Coimbatore. It started with the welcome address by Dr. S.Bhavani, HoD/ECE and continued with the benefit of this workshop. Mr.G.R.Mahendra Babu & Mr.C.Pandiarajan, Assistant Professor, Dept of ECE were the resource persons who delivered the session on the topic “Hands on Workshop on Advanced Digital Logic System Using Cadence EDA Tools” which was very useful to the participants.

Digital IC development flow is explained by Mr.G.R.Mahendra Babu. Particularly Advanced digital logic design, verilog programming and synthesis using cadence tools are explained. Hands on training are provided to the participants to explore the cadence tools. The participants were get interested in using the cadence tools.

Finally, the workshop concluded with a vote of thanks rendered by Dr. S.Bhavani, HoD/ECE and participant certificates were issued to the participants.

### **WORKSHOP PHOTO**



**G.R.Mahendra Babu, delivering a session**