

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT

Program Name	Workshop
Program Title	Two Days National Level Hands on Workshop on VLSI Design Using Cadence EDA Tools
Academic Year	2016-2017
Date & Time	13-10-2016 & 14-10-2016 (9.00 am to 5.00 pm)
Beneficiary Students	UG,PG, Research Scholars & Faculty
Resource Person Details	Mr.G.R.Mahendra Babu & Mr.M.Daranikumar, Assistant Professor, Dept of ECE , FoE(CB), Karpagam Academy of Higher Education
Organized by	Mr.G.R.Mahendra Babu & Mr.M.Daranikumar
No of Participants	60

Department of ECE organized Workshop on “Two Days National Level Hands on Workshop on VLSI Design Using Cadence EDA Tools” on 13th October 2016 & 14th October 2016. 60 candidates have participated including UG, PG & faculty members from tamilnadu. It started with the welcome address by Dr. S.Bhavani, HoD/ECE and continued with the benefit of this workshop. Mr.G.R.Mahendra Babu & Mr.M.Daranikumar, Assistant Professor, Dept of ECE were the resource persons who delivered the session on the topic “Two Days National Level Hands on Workshop on VLSI Design Using Cadence EDA Tools” which was very useful to the participants.

On day 1, Digital IC development flow is explained by Mr.G.R.Mahendra Babu. Particularly Advaced digital logic design, verilog programming and synthesis using cadence tools are explained. Hands on training are provided to the participants to explore the cadence tools. The participants were get interested in using the cadence tools.

On day 2, Mr.M.Daranikumar explained the VLSI design flow, MOSFET theory, CMOS logic styles. Particularly CMOS logic design using cadence tools are explained. Hands on training are provided to the participants to explore the cadence tools.

Finally, the workshop concluded with a vote of thanks rendered by Dr. S.Bhavani, HoD/ECE and participant certificates were issued to the participants.

About the Institution

Karpagam University goes back to 1995 when it was initiated as karpagam Arts & Science College. The college was promoted by Karpagam Charity Trust, incepted by the renowned Karpagam Industries. Today, more than 15 years later, those humble beginnings have paved the for us to emerge as a University of repute, a status granted by the MHRD and UGC (Under section 3 of UGC Act, 1956) in 2008.

The passion and endeavour to ensure excellence in education is what we believe, that has been instrumental in our evolution. Our commitment to extend world - class education permeates across the multi-disciplines we offer Arts, Science, Commerce, Management, Humanities and Engineering.



About the Location

The University is situated 10kms from Coimbatore, on the junction of Coimbatore-Pollachi National Highway and Coimbatore Bye-pass road (NH-47) Tamilnadu, INDIA

About the Department

Communication systems are transforming the way the world functions today. Innovations in networking are bringing the world together like never before. The field of Electronics & Communication is at the forefront of exciting new possibilities. At the faculty of Engineering the Dept of ECE is well geared to tune our students to the Technological revolutions in this Engineering discipline. The Departmental Labs are well equipped with recent Advanced Devices and Softwares to enhance the practical knowledge of the student and that to develop research skills in the above field.

Cadence EDA Tools

Chip design in the india poised to move into the big league with multinationals, design services companies, product companies, start-ups in the country grow by the day. Senior semiconductor executives, trade bodies such as NASSCOM, see this as a sunrise industry. They find that the only impediment is a potential shortfall of quality VLSI trained talent. Hence This Workshop strives to build the manpower pool, with the main aim of producing India a VLSI Design power house in the Global semiconductor design and development world. Cadence is committed to helping its customers by providing them with a pool of Engineers experienced in EDA tool use and methodologies


KARPAGAM UNIVERSITY
(Deemed to be University Established Under Section 3 of UGC Act, 1956)
TWO DAYS NATIONAL LEVEL
HANDS ON WORKSHOP ON
VLSI DESIGN
USING CADENCE EDA TOOLS
13 & 14 OCT 2016

 organized by
 Department of
Electronics And Communication Engineering
 Faculty of Engineering,
 KARPAGAM UNIVERSITY,
 Pollachi Main Road, Eachanari Post,
 Coimbatore - 641021,
 Tamilnadu.

Objective Of the Workshop:

Two days hands on workshop on "VLSI DESIGN USING CADENCE EDA TOOLS" focuses on the Physical design concepts, static Timing Analysis (STA), Power Analysis & Logic equivalence checking. It is useful for VLSI design engineers. Training will be based on cadence EDA tools & sessions will be handled by Cadence Design System, Bangalore.

Topics to be Covered:

- Physical Design using Encounter Digital Implementation.
- Static Timing Analysis using ETS.
- Power Analysis using EPS Engine.
- Logic equivalence checking using conformal LEC.
- Case Study

Resource Person:

- Engineers from Cadence Design System, Bangalore & Academic People.

Important Dates:

- Last date for Payment: 10-10-2016
- Initiation of Confirmation: 11-10-2016
- Duration of program: 13th & 14th OCT 2016

Registration & Payment Mode

The Participants willing to join must register their names by sending the filled Registration Form. Only 30 Participants are allowed on first come first serve basis.

The registration form is prescribed format along with DD drawn in favour of "KARPAGAM UNIVERSITY" payable at coimbatore, is sent to the below mentioned contact address on or before 10th Oct 2016.

Candidates can also register using the below google form
<https://goo.gl/forms/8lqpdA6M14ff1c14V1>

Fee

- Students (UG & PG): Rs.750/-
- Faculty / Research Scholars: Rs. 1000/-
- Industry Participants: Rs.1200/-

Registration fee includes registration kit, Certificate, lunch and refreshments.

Accommodation will be provided on payment basis.

Organising committee

- Chief Patron :** Dr. R. Vasanthakumari,
President, Karpagam University
- Patron :** Shri. K. Marudiah,
Adviser, Karpagam University
Dr.S.Sudalaiimuthu,
Vice Chancellor, Karpagam University
Dr.G.Sekar,
Registrar, Karpagam University
- Convener :** Dr.R.Sundararajan
Dean, Faculty of Engineering
Karpagam University
- Co-Convener :** Dr.S.Bharani
HOD/ECE, Faculty of Engineering,
Karpagam University

Coordinators

- Mr.G.R.Mahendrababu - +91 9965529279
Mr.M.Dharani Kumar - +91 9843532202
Assistant Professor, Department of ECE/POE,
Karpagam University.

Address for Communication

Dr.S.Bharani, HOD/ECE,
Faculty of Engineering (CE),
Karpagam University,
Pollachi main road, Echaturai Post,
Coimbatore - 641021, Tamilnadu, INDIA.

TWO DAYS NATIONAL LEVEL HANDS ON WORKSHOP ON VLSI DESIGN USING CADENCE EDA TOOLS

13 & 14 OCT 2016

REGISTRATION FORM

Name.....
Designation.....
Department.....
Institution.....
Organization.....
Address.....

Phone/Mobile.....
Email ID.....
DOB/Particulars.....

Date:..... Signature:.....

Sponsorship certificate from the Head of the Institution/Industry

This is to certify that Mr/Ms.....
of.....It sponsored
to participate in the two days National level Hands on
workshop on "VLSI Design Using Cadence EDA Tools"
on 13th & 14th October 2016.

Head of the Institution

WORKSHOP PHOTO



M.Darani kumar, delivering a session