19MMU504/19CHU503

**3** PHYSICS – II

4H - 4C

Instruction Hours / week: L: 4 T: 0 P: 0

Marks: Internal: 40

External: 60 Total: 100

End Semester Exam: 3 Hours

#### **Course Objectives:**

- To give the basic knowledge on material properties.
- To acquire knowledge on magnetism and digital electronics.
- To educate and motivate the students in the field of science.

## **Course Outcomes:**

### Students can able to

- Explain how physics applies to phenomena in the world around them.
- Recognize how and when physics methods and principles can help address problems in their major and then apply those methods and principles to solve problems.

### UNIT – I

**Electrostatics:** Coulombs law – electric field – Gauss's law and its applications – potential – potential due to various charge distribution. Parallel plate capacitors – dielectrics- current – galvanometer – voltmeter – ammeter- potentiometric measurements.

## UNIT - II

**Magnetism:** Magnetic field – Biot Savart's law – B due to a solenoid – Amperes law – Faradays law of induction – Lenz's law. Magnetic properties of matter –Dia, para and ferro - Cycle of magnetization – Hysteresis – B-H curve – Applications of B-H curve.

### UNIT - III

**Modern Physics:** Einstein's Photoelectric effect-characteristics of photoelectron –laws of photoelectric emission-Einstein's photo electric equations- Compton effect-matter waves-De-Broglie Hypothesis. Heisenberg's uncertainty principle-Schrödinger's equation- particle in a box.

#### **UNIT-IV**

Atomic and Nuclear Physics: Atom Models : Sommerfield's and Vector atom Models – Pauli's exclusion Principle – Various quantum numbers and quantization of orbits. Xrays : Continuous and Characteristic X-rays – Mosley's Law and importance – Bragg's Law.

Nuclear forces –characteristics - nuclear structure by liquid drop model – Binding energy – mass defect – particle accelerators – cyclotron and betatron – nuclear Fission and nuclear Fusion.

#### UNIT - V

**Digital Electronics:** Decimal – binary – octal and hexadecimal numbers– their representation, inter-conversion, addition and subtraction, negative numbers. Sum of products – product of sums – their conversion – Simplification of Boolean expressions - K-Map – min terms – max terms - (2, 3 and 4 variables). Basic logic gates – AND, OR, NOT, NAND, NOR and EXOR gates – NAND and NOR as universal building gates – Boolean Algebra – Laws of Boolean Algebra – De Morgan's Theorems – Their verifications using truth tables.

#### SUGGESTED READINGS

1. Narayanamurthi, Electricity and Magnetism, The National Publishing Co, First edition, 1988.

2. J. B. Rajam, Atomic Physics., S. Chand & Company Limited, New Delhi, First edition, 1990.

3. B. N. Srivastava, Basic Nuclear Physic, Pragati Prakashan, Meerut, 2005.

4. Albert Paul Malvino, Digital principles and Applications, McGraw-Hill International Editions, New York, 2002.

5. Digital fundamentals – by Floyd 8th edition Pearson education 2006

6. R. S. Sedha, A text book of Digital Electronics, S. Chand & Co, New Delhi, First edition ,2004.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

## UNIT – I

**Electrostatics:** Coulombs law – electric field – Gauss's law and its applications – potential – potential due to various charge distribution. Parallel plate capacitors – dielectrics- current – galvanometer – voltmeter – ammeter- potentiometric measurements.

## **Electrostatics:**

## Coulomb's law

The force between two charged bodies was studied by Coulomb in 1785.Coulomb's law states that the force of attraction or repulsion between two point charges is directly proportional to the product of the charges and inversely proportional to the square of the distance between them. The direction of forces is alongthe line joining the two point charges.

One Coulomb is defined as the quantity of charge, which when placed at a distance of 1 metre in air or vacuum from an equal and similar charge, experiences a repulsive force of  $9 \times 10^9$  N. The forces exerted by charges on each other are equal in magnitude and opposite in direction.

## **Electric Field**

Electric field due to a charge is the space around the test charge in which it experiences a force. The presence of an electric field around a charge cannot be detected unless another charge is brought towards it.

When a test charge  $q_o$  is placed near a charge q, which is the source of electric field, an electrostatic force F will act on the test charge.

## Gauss's law

The law relates the flux through any closed surface and the netcharge enclosed within the surface. The law states that the total flux

1

of the electric field E over any closed surface is equal to  $\varepsilon_{o}$  times the net charge enclosed by the surface.

q



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

#### $\varphi = \varepsilon_{o}$

This closed imaginary surface is called Gaussian surface. Gauss's law tells us that the flux of E through a closed surface S depends only on the value of net charge inside the surface and not on the location of the charges. Charges outside the surface will not contribute to flux.

### Applications of Gauss's Law

## i) Field due to an infinite long straight charged wire

Consider an uniformly charged wire of infinite length having a constant linear charge density  $\lambda$  (charge per unit <sup>E</sup> length). Let P be a point at a distance r from the wire (Fig. 1.17) and E be the electric field at the point P. A cylinder of length *l*, radius r, closed at each end by plane caps normal to the axis is chosen as Gaussian surface. Consider a very *Fig 1.17 Infinitely long* small

area *ds* on the Gaussian surface. *straight charged wire* By symmetry, the magnitude of the electric field will be the same at all points on the curved surface of the cylinder and directed radially

outward.  $_E$  and ds are along the same direction.

The electric flux ( $\varphi$ ) through curved surface =  $\int E ds \cos \theta$ 

(: The surface area of the curved part is  $2\pi$  rl)

Since  $_E$  and ds are right angles to each other, the electric flux through the plane caps = 0

 $\therefore$  Total flux through the Gaussian surface,  $\varphi = E$ . (2 $\pi$ rl)

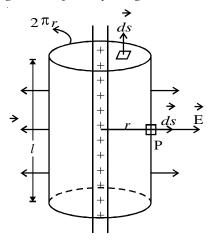
The net charge enclosed by Gaussian surface is,  $q = \lambda I$ 

∴ By Gauss's law,

 $\lambda l$  \_ $\lambda$  \_

 $E(2\pi rl) = \varepsilon o \text{ or } E = 2\pi\varepsilon o r$ 

The direction of electric field E is radially outward, if line charge is positive and inward, if the line charge is negative.





### Electric field due to an infinite charged plane sheet

Consider an infinite plane sheet of charge with surface charge density  $\sigma$ . Let P be a point at a distance *r* from the sheet (Fig. 1.18) and E be the electric field at P. Consider a Gaussian surface in the form of cylinder of cross- sectional area A and length 2*r* perpendicular to the sheet of charge. *Fig* 1.18 *Infinite plane sheet* By symmetry, the electric field is at right angles to the end caps and away from the plane. Its magnitude is the same at P and at the other cap at P'. Therefore, the total flux through the closed surface is given by

 $\varphi = \iiint E \, ds. \qquad \exists P + \iiint E \, ds. \qquad \exists P + \iiint E \, E \, ds. \qquad \exists P + \iiint E \, E \, ds. \qquad \exists P + \iiint E \, E \, ds. \qquad \exists P + \iiint E \, E \, ds. \qquad \exists P + \iiint E \, E \, ds. \qquad \exists P + \iiint E \, ds. \qquad \exists P + \boxtimes E \, ds. \quad ds.$ 

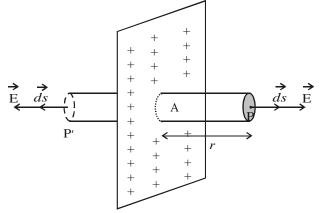
Using Gauss's law,

 $\sigma A \ 2 \ \mathbf{E} \ \mathbf{A} = \varepsilon o$ 

```
σ
```

## $:: \mathbf{E} = {}_{2\varepsilon o}$

#### Capacitance of a conductor



When a charge q is given to an isolated conductor, its potential will change. The change in potential depends on the size and shape of the conductor. The potential of a

conductor changes by V, due to the charge q given to the conductor.  $q\alpha$  V or q = CV

i.e. C = q/V

Here C is called as capacitance of the conductor.

The capacitance of a conductor is defined as the ratio of the charge given to the conductor to the potential developed in the conductor.

The unit of capacitance is farad. A conductor has a capacitance of one farad, if a charge of 1 coulomb given to it, rises its potential by 1 volt.

The practical units of capacitance are  $\mu F$  and pF.



### Principle of a capacitor

Consider an insulated conductor (Plate A) with a positive charge 'q' having potential V (Fig 1.22a). The capacitance of A is C = q/V. When another insulated metal plate B is brought near A, negative charges are induced on the side of B near A. An equal amount of positive charge is induced on the other side of B (Fig 1.22b). The negative charge in B decreases the potential of A. The positive charge in B increases the potential of A. But the negative charge on B is nearer to A than the positive charge on B. So the net effect is that, the potential of A decreases. Thus the capacitance of A is increased.

If the plate B is		earthed,	,			positive charges get
neutralized (Fig	1.22c).	Then	the			potential of A
decreases	further.	Thus	the			capacitance of A is
considerably		increased.				
The capacitance		depends	on	the		geometry of the
conductors and	nature	of	the			medium. A capacitor
is a device for	storing					electriccharges.
А	-	A	A			B A
В			I	I	Ŧ	
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						
++ -++ -						

Fig 1.22 Principle of capacitor

## Capacitance of a parallel plate capacitor

The parallel plate capacitor consists of two parallel metal plates X and Y each of area A, separated by a distance *d*, having a surface charge



density  $\sigma$  (fig. 1.23). The medium

### -q

+q is given to the plate X. It induces

a charge -q on the upper surface of <sup>*Fig 1.23 Parallel plate capacitor* earthed plate Y. When the plates are very close to each other, the field is confined to the region between them. The electric lines of force starting from plate X and ending at the plate Y are parallel to each other and perpendicular to the plates.</sup>

By the application of Gauss's law, electric field at a point between the two plates is,

σ  $E = \varepsilon o$ Potential difference between the plates X and Y is 0 0 σ  $\sigma d$  $\frac{q}{V} = \frac{-\sigma A}{\sigma d/\epsilon_o} = \frac{-\epsilon_o A}{d} \quad \overline{\epsilon_o} dr = \epsilon_o$ V = [-E dr = -]d  $\epsilon_o A$ (C) of the parallel plate capacitor The capacitance q  $C = [since, \sigma = ]$ A C =*:*.

The capacitance is directly proportional to the area (A) of the plates and inversely proportional to their distance of separation (d).

## Dielectrics and polarization

## Dielectrics

A dielectric is an insulating material in which all the electrons are tightly bound to the nucleus of the atom. There are no free electrons to carry current. Ebonite, mica and oil are few examples of dielectrics. The electrons are not free to move under the influence of an external field.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

## UNIT – III

**Modern Physics:** Einstein's Photoelectric effect-characteristics of photoelectron –laws of photoelectric emission-Einstein's photo electric equations- Compton effect-matter waves-De-Broglie Hypothesis. Heisenberg's uncertainty principle-Schrödinger's equation- particle in a box.

## **Modern Physics**

### **Einstein's Photoelectric effect**

The photoelectric effect is a phenomenon where electrons are emitted from the metal surface when the light of sufficient frequency is incident upon. The concept of photoelectric effect was first documented in 1887 by Heinrich Hertz and later by Lenard in 1902. But both the observations of the photoelectric effect could not be explained by Maxwell's electromagnetic wave theory of light. Hertz (who had proved the wave theory) himself did not pursue the matter as he felt sure that it could be explained by the wave theory. It, however, failed on the following accounts:

According to the wave theory, energy is uniformly distributed across the wavefront and is dependent only on the intensity of the beam. This implies that the kinetic energy of electrons increases with light intensity. However, the kinetic energy was independent of light intensity. Wave theory says that light of any frequency should be capable of ejecting electrons. But

electron emission occurred only for frequencies larger than a threshold frequency (v0). Since energy is dependent on intensity according to wave theory, the low-intensity light should emit electrons after some time so that the electrons can acquire sufficient energy to get emitted. However, electron emission was spontaneous no matter how small the intensity of light.

Following is the table with link of other experiment related to photoelectric effect:

### Einstein's explanation of Photoelectric effect

Einstein resolved this problem using Planck's revolutionary idea that light was a particle. The energy carried by each particle of light (called quanta or photon) is dependent on the light's frequency (v) as shown:

#### $\mathbf{E} = \mathbf{h}\mathbf{v}$

### Where h = Planck's constant = $6.6261 \times 10-34$ Js.

Since light is bundled up into photons, Einstein theorized that when a photon falls on the surface of a metal, the entire photon's energy is transferred to the electron.



A part of this energy is used to remove the electron from the metal atom's grasp and the rest is given to the ejected electron as kinetic energy. Electrons emitted from underneath the metal surface lose some of the kinetic energy during the collision. But the surface electrons carry all the kinetic energy imparted by the photon and have the maximum kinetic energy. We can write this mathematically as:

Energy of photon = energy required to eject electron ( work function) + Maximum kinetic energy of the electron E = W + KEhv = W + KEKE = hv - w

At the threshold frequency v0 electrons are just ejected and do not have any kinetic energy. Below this frequency there is no electron emission. Thus, the energy of a photon with this frequency must be the work function of the metal.

 $\mathbf{w} = hv_0$ Thus, Maximum kinetic energy equation becomes:  $\mathbf{KE} = 12mv2max=hv-hv_0$  $12mv2max=h(v-v_0)$ 

Vmax is the maximum kinetic energy of the electron. It is calculated experimentally using the stopping potential. Please read our article on Lenard's observations to understand this part.

#### **Stopping potential =** $ev_0 = 12mv2max$

Thus, Einstein explained the Photoelectric effect by using the particle nature of light. Stay tuned with BYJU'S to learn more about the photoelectric effect along with engaging video lectures.

**Compton effect,** increase in wavelength of X-rays and other energetic electromagnetic radiations that have been elastically scattered by electrons; it is a principal way in which radiant energy is absorbed in matter. The effect has proved to be one of the cornerstones of quantum mechanics, which accounts for both wave and particle properties of radiation as well as of matter.

#### Matter waves

It is **the wave** formed by **matter**, or in another word, particles. Precisely speaking, every **matter** formed by particles, or just particles like electrons, have **wave**-like **property**,



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

which means they can behave both like particles and **waves**. It is only when particles move that they have **wave**-like **property** 

### **De Broglie's Thesis**

In his 1923 (or 1924, depending on the source) doctoral dissertation, the French physicist Louis de Broglie made a bold assertion. Considering Einstein's relationship of wavelength *lambda* to momentum p, de Broglie proposed that this relationship would determine the wavelength of any matter, in the relationship:

lambda = h / p

recall that *h* is Planck's constant

This wavelength is called the *de Broglie wavelength*. The reason he chose the momentum equation over the energy equation is that it was unclear, with matter, whether *E* should be total energy, kinetic energy, or total relativistic energy. For photons, they are all the same, but not so for matter.

Assuming the momentum relationship, however, allowed the derivation of a similar de Broglie relationship for frequency f using the kinetic energy  $E_k$ :

 $f = E_k / h$ 

## **Alternate Formulations**

De Broglie's relationships are sometimes expressed in terms of Dirac's constant, h-bar = h / (2pi), and the angular frequency w and wavenumber k:

 $p = h - bar * kE_k$ = h - bar \* w

## **Experimental Confirmation**

In 1927, physicists Clinton Davisson and Lester Germer, of Bell Labs, performed an experiment where they fired electrons at a crystalline nickel target. The resulting diffraction pattern matched the predictions of the de Broglie wavelength. De Broglie received the 1929 Nobel Prize for his theory (the first time it was ever awarded for a Ph.D. thesis) and Davisson/Germer jointly won it in 1937 for the experimental discovery of electron diffraction (and thus the proving of de Broglie's hypothesis).

Further experiments have held de Broglie's hypothesis to be true, including the quantum variants of the double slit experiment. Diffraction experiments in 1999 confirmed the de Broglie wavelength for the behavior of molecules as large as buckyballs, which are complex molecules made up of 60 or more carbon atoms.

### Significance of the de Broglie Hypothesis

The de Broglie hypothesis showed that wave-particle duality was not merely an aberrant behavior of light, but rather was a fundamental principle exhibited by both radiation and matter. As such, it becomes possible to use wave equations to describe material behavior, so long as one



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

properly applies the de Broglie wavelength. This would prove crucial to the development of quantum mechanics. It is now an integral part of the theory of atomic structure and particle physics.

## Macroscopic Objects and Wavelength

Though de Broglie's hypothesis predicts wavelengths for matter of any size, there are realistic limits on when it's useful. A baseball thrown at a pitcher has a de Broglie wavelength that is smaller than the diameter of a proton by about 20 orders of magnitude.

The Heisenberg uncertainty principle states that it is impossible to know simultaneously the exact position and momentum of a particle. That is, the more exactly the position is determined, the less known the momentum, and vice versa. This principle is not a statement about the limits of technology, but a fundamental limit on what can be known about a particle at any given moment. This uncertainty arises because the act of measuring affects the object being measured. The only way to measure the position of something is using light, but, on the sub-atomic scale, the interaction of the light with the object inevitably changes the object's position and its direction of travel.

## The Schrödinger Equation and a Particle in a Box

The particle in a box model (also known as the infinite potential well or the infinite square well) describes a particle free to move in a small space surrounded by impenetrable barriers. The model is mainly used as a hypothetical example to illustrate the differences between classical and quantum systems. In classical systems, for example a ball trapped inside a large box, the particle can move at any speed within the box and it is no more likely to be found at one position than another. However, when the well becomes very narrow (on the scale of a few nanometers), quantum effects become important. The particle may only occupy certain positive energy levels. The particle in a box model provides one of the very few problems in quantum mechanics which can be solved analytically, without approximations. This means that the observable properties of the particle (such as its energy and position) are related to the mass of the particle and the width of the well by simple mathematical expressions. Due to its simplicity, the model allows insight into quantum effects without the need for complicated mathematics. It is one of the first quantum mechanics problems taught in undergraduate physics courses, and it is commonly used as an approximation for more complicated quantum systems.

A particle in a 1-dimensional box is a fundamental quantum mechanical approximation describing the translational motion of a single particle confined inside an infinitely deep well from which it *cannot* escape.

The particle in a box problem is a common application of a quantum mechanical model to a simplified system consisting of a particle moving horizontally within an infinitely deep well from which it cannot escape. The solutions to the problem give possible values of E and  $\psi\psi$  that



(Deemed to be University)

(Established Under Section 3 of UGC Act. 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

(Established Under Section 3 of UGC Act, 1956 )

the particle can possess. E represents allowed energy values and  $\psi(x)\psi(x)$  is a wavefunction, which when squared gives us the probability of locating the particle at a certain position within the box at a given energy level.

To solve the problem for a particle in a 1-dimensional box, we must follow our **Big**, **Big recipe** for Quantum Mechanics:

Define the Potential Energy, V Solve the Schrödinger Equation Define the wavefunction Define the allowed energies Step 1: Define the Potential Energy V 00 ŝ V4 0 ×

A particle in a 1D infinite potential well of dimension LL.

The potential energy is 0 inside the box (V=0 for 0<x<L) and goes to infinity at the walls of the box (V= $\infty$  for x<0 or x>L). We assume the walls have infinite potential energy to ensure that the particle has zero probability of being at the walls or outside the box. Doing so significantly simplifies our later mathematical calculations as we employ these boundary conditions when solving the Schrödinger Equation.

Step 2: Solve the Schrödinger Equation

The time-independent Schrödinger equation for a particle of mass *m* moving in one direction with energy E is

 $-\hbar 22 \text{md} 2\psi(x) dx 2 + V(x)\psi(x) = E\psi(x)(1)(1) - \hbar 22 \text{md} 2\psi(x) dx 2 + V(x)\psi(x) = E\psi(x)$ 

with

 $\hbar\hbar$  is the reduced Planck Constant where  $\hbar=h2\pi\hbar=h2\pi$ m is the mass of the particle  $\psi(x)\psi(x)$  is the stationary time-independent wavefunction V(x) is the potential energy as a function of position EE is the energy, a real number

0



This equation can be modified for a particle of mass m free to move parallel to the x-axis with zero potential energy (V = 0 everywhere) resulting in the quantum mechanical description of free motion in one dimension:

 $-\hbar 22 \text{md} 2\psi(x) dx 2 = E\psi(x)(2)(2) - \hbar 22 \text{md} 2\psi(x) dx 2 = E\psi(x)$ 

This equation has been well studied and gives a general solution of:

 $\psi(x) = Asin(kx) + Bcos(kx)(3)(3)\psi(x) = Asin[10](kx) + Bcos[10](kx)$ 

where A, B, and k are constants.

Step 3: Define the wavefunction

The solution to the Schrödinger equation we found above is the general solution for a 1dimensional system. We now need to apply our **boundary conditions** to find the solution to our particular system. According to our boundary conditions, the probability of finding the particle at x=0 or x=L is zero. When x=0x=0, sin(0)=0sin(0)=0, and cos(0)=1cos(0)=1; therefore, *B must equal 0* to fulfill this boundary condition giving:  $\psi(x)=Asin(kx)(4)(4)\psi(x)=Asin(0)kx)$ 

We can now solve for our constants (A and k) systematically to define the wavefunction.

### Solving for k

Differentiate the wavefunction with respect to x:

 $d\psi dx = kA\cos(kx)(5)(5)d\psi dx = kA\cos(kx)$  $d2\psi dx = -k2A\sin(kx)(6)(6)d2\psi dx = -k2A\sin(kx)$ Since  $\psi(x) = A\sin(kx)\psi(x) = A\sin(kx)$ , then

 $d2\psi dx2 = -k2\psi(7)(7)d2\psi dx2 = -k2\psi$ 

If we then solve for k by comparing with the Schrödinger equation above, we find:

k=(8π2mEh2)1/2(8)(8)k=(8π2mEh2)1/2

Now we plug k into our wavefunction:

 $\psi = Asin(8\pi 2mEh2)1/2x(9)(9)\psi = Asin^{10}(8\pi 2mEh2)1/2x$ 



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

## Solving for A

To determine A, we have to apply the boundary conditions again. Recall that the *probability of finding a particle at* x = 0 or x = L is zero.

When x=Lx=L:

 $0=Asin(8\pi 2mEh2)1/2L(10)(10)0=Asin[10](8\pi 2mEh2)1/2L$ This is only true when  $(8\pi 2mEh2)1/2L=n\pi(11)(11)(8\pi 2mEh2)1/2L=n\pi$ where n = 1,2,3...

Plugging this back in gives us:

 $\psi$ =Asinn $\pi$ Lx(12)(12) $\psi$ =Asin<sup>[10]</sup>n $\pi$ Lx To determine AA, recall that the total probability of finding the particle inside the box is 1, meaning there is no probability of it being outside the box. When we find the probability and set it equal to 1, we are *normalizing* the wavefunction.  $\int L0\psi 2dx = 1(13)(13)\int 0L\psi 2dx = 1$ 

For our system, the normalization looks like:

A2  $\int L0\sin^2(n\pi xL)dx = 1(14)(14)A2 \int 0L\sin^2(n\pi xL)dx = 1$ Using the solution for this integral from an integral table, we find our normalization constant, AA: A=2L-- $\sqrt{(15)(15)A=2L}$ 

Which results in the normalized wavefunction for a particle in a 1-dimensional box:

 $\psi=2L--\sqrt{\sin \pi Lx(16)(16)\psi}=2L\sin \frac{\pi Lx}{16}$ Step 4: Determine the Allowed Energies

Solving for E results in the allowed energies for a particle in a box:

En=n2h28mL2(17)(17)En=n2h28mL2 This is an important result that tells us:

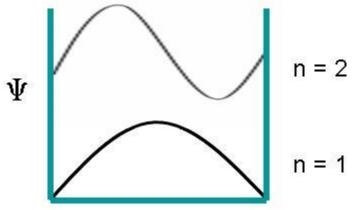
The energy of a particle is quantized and

The lowest possible energy of a particle is **NOT** zero. This is called the **zero-point energy** and means the particle can **never be at rest** because it always has some kinetic energy.

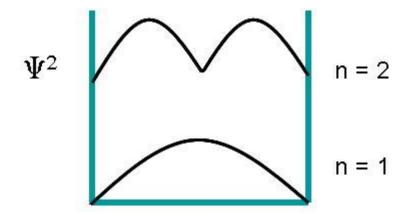


This is also consistent with the Heisenberg Uncertainty Principle: if the particle had zero energy, we would know where it was in both space and time.

The wavefunction for a particle in a box at the n=1n=1 and n=2n=2 energy levels look like this:



The probability of finding a particle a certain spot in the box is determined by squaring  $\psi\psi$ . The probability distribution for a particle in a box at the n=1n=1 and n=2n=2 energy levels looks like this:



Notice that the number of **nodes** (places where the particle has zero probability of being located) increases with increasing energy n. Also note that as the energy of the particle becomes greater, the quantum mechanical model breaks down as the energy levels get closer together and overlap, forming a continuum. This continuum means the particle is free and can have any energy value. At such high energies, the classical mechanical model is applied as the particle behaves more like a continuous wave. Therefore, the particle in a box problem is an example of Wave-Particle Duality.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

## IMPORTANT FACTS TO LEARN FROM THE PARTICLE IN THE BOX

- The energy of a particle is quantized. This means it can only take on discrete energy values.
- The lowest possible energy for a particle is **NOT** zero (even at 0 K). This means the particle *always* has some kinetic energy.
- The square of the wavefunction is related to the probability of finding the particle in a specific position for a given energy level.
- The probability changes with increasing energy of the particle and depends on the position in the box you are attempting to define the energy for.
- In classical physics, the probability of finding the particle is independent of the energy and the same at all points in the box.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

## $\mathbf{UNIT} - \mathbf{V}$

## **Digital Electronics**

**Digital Electronics:** Decimal – binary – octal and hexadecimal numbers– their representation, inter-conversion, addition and subtraction, negative numbers. Sum of products – product of sums – their conversion – Simplification of Boolean expressions - K-Map – min terms – max terms - (2, 3 and 4 variables). Basic logic gates – AND, OR, NOT, NAND, NOR and EXOR gates – NAND and NOR as universal building gates – Boolean Algebra – Laws of Boolean Algebra – De Morgan's Theorems – Their verifications using truth tables.

### **Number Systems**

There are infinite ways to represent a number. The four commonly associated with modern computers and digital electronics are: decimal, binary, octal, and hexadecimal.

**Decimal** (base 10) is the way most human beings represent numbers. Decimal is sometimes abbreviated as dec.

Decimal counting goes:

0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, and so on.

**Binary** (base 2) is the natural way most digital circuits represent and manipulate numbers. (Common misspellings are "bianary", "bienary", or "binery".) Binary numbers are sometimes represented by preceding the value with '0b', as in 0b1011. Binary is sometimes abbreviated as bin.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Binary counting goes:

0, 1, 10, 11, 100, 101, 110, 111, 1000, 1001, 1010, 1011, 1100, 1101, 1110, 1111, 10000, 10001, and so on.

**Octal** (base 8) was previously a popular choice for representing digital circuit numbers in a form that is more compact than binary. Octal is sometimes abbreviated as oct.

Octal counting goes:

0, 1, 2, 3, 4, 5, 6, 7, 10, 11, 12, 13, 14, 15, 16, 17, 20, 21, and so on.

**Hexadecimal** (base 16) is currently the most popular choice for representing digital circuit numbers in a form that is more compact than binary. (Common misspellings are "hexdecimal", "hexidecimal", "hexedecimal", or "hexodecimal".) Hexadecimal numbers are sometimes represented by preceding the value with '0x', as in 0x1B84. Hexadecimal is sometimes abbreviated as hex.

Hexadecimal counting goes:

0, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F, 10, 11, and so on.

All four number systems are equally capable of representing any number. Furthermore, a number can be perfectly converted between the various number systems without any loss of numeric value.

At first blush, it seems like using any number system other than human-centric decimal is complicated and unnecessary. However, since the job of electrical and software engineers is to work with digital circuits, engineers require number systems that can best transfer information between the human world and the digital circuit world.

It turns out that the way in which a number is represented can make it easier for the engineer to perceive the meaning of the number as it applies to a digital circuit. In other words, the appropriate number system can actually make things less complicated.



#### **Fundamental Information Element of Digital Circuits**

Almost all modern digital circuits are based on two-state switches. The switches are either on or off. It doesn't matter if the switches are actually physical switches, vacuum tubes, relays, or transistors. And, it doesn't matter if the 'on' state is represented by 1.8 V on a cutting-edge CPU core, -12 V on a RS-232 interface chip, or 5 V on a classic TTL logic chip.

Because the fundamental information element of digital circuits has two states, it is most naturally represented by a number system where each individual digit has two states: binary. For example, switches that are 'on' are represented by '1' and switches that are 'off' are represented by '0'. It is easy to instantly comprehend the values of 8 switches represented in binary as 10001101. It is also easy to build a circuit to display each switch state in binary, by having an LED (lit or unlit) for each binary digit.

#### **Conversion of Numbers**

Conversion of numbers from one system to another becomes necessary to understand the process and the logic of the operations of a computer system. It is not very difficult to convert numbers from one base to another. We will first discuss about the conversion of binary numbers to their decimal equivalents.

### (i) Expansion Method:

In expansion method the conversion of binary numbers to their decimal equivalents are shown with the help of the examples.

1. Convert the decimal numbers to their binary equivalents:

(a) 256

### Solution:

256



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

<mark>256</mark>	128	64	32	16	8	4	2	1
1	0	0	0	0	0	0	0	0

Since the given number 256 appears in the first row, we put 1 in the slot below 256 and fill all

the other slots to the right of this slot with zeros.

Thus,  $256_{10} = 10000000_2$ 

Addition and subtraction of octal numbers are explained using different examples.

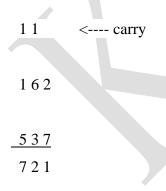
### Addition of octal numbers:

Addition of octal numbers is carried out by the same principle as that of decimal or binary numbers.

### **Evaluate:**

(i) (162)<sub>8</sub> + (537)<sub>8</sub>

## Solution:



Therefore, sum =  $721_8$ 

(ii) (136) <sub>8</sub> + (636) <sub>8</sub>



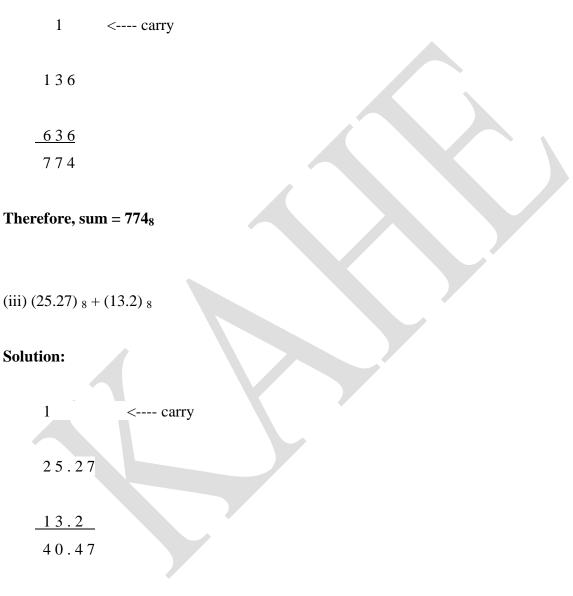
(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Solution:



Therefore, sum =  $(40.47)_8$ 

(iv) (67.5) <sub>8</sub> + (45.6) <sub>8</sub>



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

11	< carry
67.5	
<u>45.6</u> 135.3	
Therefore, sum =	(135.3) 8
(b) 77	
Solution:	
77	
(b) 77	
Solution:	
77	

The given number is less than 128 but greater than 64. We therefore put 1 in the slot corresponding to 64 in the first row. Next, we subtract 64 from 77 and get 13 as remainder.

This remainder is less than 16 and greater than 8. So we put 1 in the slot corresponding to 8 and subtract 8 from 13. This gives 13 - 8 = 5. This remainder is greater than 4 and less than 8.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Hence we put 1 in the slot corresponding to 4 and subtracting 4 from 5 we get 1. Now, 1

is present in the right hand most slot of the first row. We, therefore, put 1 in the corresponding slot and fill all other slots with zeros.

Thus, 7710 = 10011012.

Conversion of decimal fractions to binary fractions may also be accomplished by using similar method. Let us observe the procedure with the help of the following example:

2. Convert 0.67510 to its binary equivalent.

Solution:

## **Convert Decial Number to Binary Number**

Subtract .5 from the given number to get .675 - .5 = .175 and place 1 in the slot corresponding to .5 of the first row.

Now the number .175 is less than .25 and greater than .125. So, we put 1 in the slot corresponding to the number .125 of the first row and subtract .125 from .175 to get .175 - .125 = .05. The remainder .05 is less than .0625 but greater than .03125.

Hence we put 1 in the slot corresponding to 0.3125 and the subtraction given .05 - .03125 = .01875 and continue the process. The other slots are then filled with zeros.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

64	32	16	8	4	2	1
1	0	0	1	1	0	1

## Thus, .67510 = (.10101...)2

The given number is less than 128 but greater than 64. We therefore put 1 in the slot corresponding to 64 in the first row. Next, we subtract 64 from 77 and get 13 as remainder. This remainder is less than 16 and greater than 8. So we put 1 in the slot corresponding to 8 and subtract 8 from 13. This gives 13 - 8 = 5. This remainder is greater than 4 and less than 8. (b) 77

### Solution:

77

The given number is less than 128 but greater than 64. We therefore put 1 in the slot corresponding to 64 in the first row. Next, we subtract 64 from 77 and get 13 as remainder.

This remainder is less than 16 and greater than 8. So we put 1 in the slot corresponding to 8 and subtract 8 from 13. This gives 13 - 8 = 5. This remainder is greater than 4 and less than 8.

Hence we put 1 in the slot corresponding to 4 and subtracting 4 from 5 we get 1. Now, 1 is present in the right hand most slot of the first row. We, therefore, put 1 in the corresponding slot and fill all other slots with zeros.

Thus, 7710 = 10011012.

Department of Physics, KAHE.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Conversion of decimal fractions to binary fractions may also be accomplished by using similar method. Let us observe the procedure with the help of the following example:

## 2. Convert 0.67510 to its binary equivalent.

### Solution:

## **Convert Decial Number to Binary Number**

Subtract .5 from the given number to get .675 - .5 = .175 and place 1 in the slot corresponding to .5 of the first row.

Now the number .175 is less than .25 and greater than .125. So, we put 1 in the slot corresponding to the number .125 of the first row and subtract .125 from .175 to get .175 - .125 = .05. The remainder .05 is less than .0625 but greater than .03125.

Hence we put 1 in the slot corresponding to 0.3125 and the subtraction given .05 - .03125 = .01875 and continue the process. The other slots are then filled with zeros.

Thus, .67510 = (.10101...)2 Solution:77

The given number is less than 128 but greater than 64. We therefore put 1 in the slot corresponding to 64 in the first row. Next, we subtract 64 from 77 and get 13 as remainder.

This remainder is less than 16 and greater than 8. So we put 1 in the slot corresponding to 8 and subtract 8 from 13. This gives 13 - 8 = 5. This remainder is greater than 4 and less than 8.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Hence we put 1 in the slot corresponding to 4 and subtracting 4 from 5 we get 1. Now, 1 is present in the right hand most slot of the first row. We, therefore, put 1 in the corresponding slot and fill all other slots with zeros.

Thus, 7710 = 10011012.

Conversion of decimal fractions to binary fractions may also be accomplished by using similar method. Let us observe the procedure with the help of the following example:

## 2. Convert 0.67510 to its binary equivalent.

### Solution:

## **Convert Decial Number to Binary Number**

Subtract .5 from the given number to get .675 - .5 = .175 and place 1 in the slot corresponding to .5 of the first row.

Now the number .175 is less than .25 and greater than .125. So, we put 1 in the slot corresponding to the number .125 of the first row and subtract .125 from .175 to get .175 - .125 = .05. The remainder .05 is less than .0625 but greater than .03125.

Hence we put 1 in the slot corresponding to 0.3125 and the subtraction given .05 - .03125 = .01875 and continue the process. The other slots are then filled with zeros.

Thus,  $.67510 = (.10101...)_2$ 

## Subtraction of octal numbers:

Similarly, subtraction of octal numbers can be performed by following the rules of subtraction of decimal numbers.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Thus, for performing addition and subtraction of octal numbers we can follow the rules of addition and subtraction of decimal numbers.

Boolean functions may be practically implemented by using electronic gates. The following points are important to understand.

Electronic gates require a power supply.

Gate **INPUTS** are driven by voltages having two nominal values, e.g. 0V and 5V representing logic 0 and logic 1 respectively.

The **OUTPUT** of a gate provides two nominal values of voltage only, e.g. 0V and 5V representing logic 0 and logic 1 respectively. In general, there is only one output to a logic gate except in some special cases.

There is always a time delay between an input being applied and the output responding.

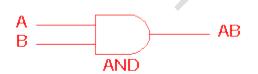
### **Truth Tables**

<u>Truth tables</u> are used to help show the function of a logic gate. If you are unsure about <u>truth tables</u> and need guidence on how go about drawningthem for individual gates or logic circuits then use the <u>truth table</u> section link.

Logic gates

Digital systems are said to be constructed by using logic gates. These gates are the AND, OR, NOT, NAND, NOR, EXOR and EXNOR gates. The basic operations are described below with the aid of <u>truth tables</u>.

## AND gate



2 Input AND gate						
A B A.B						
0	0	0				
0	1	0				
1	0	0				
1	1	1				

The AND gate is an electronic circuit that gives a **high** output (1) only if **all** its inputs are high. A dot (.) is used to show the AND operation i.e. A.B. Bear in mind that this dot is sometimes omitted i.e. AB



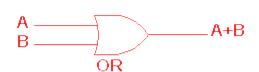
(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

### OR gate



]	2 Input OR gate					
	A+B	В	А			
	0	0	0			
	1	1	0			
	1	0	1			
	1	1	1			

The OR gate is an electronic circuit that gives a high output (1) if **one or more** of its inputs are high. A plus (+) is used to show the OR operation.

### NOT gate

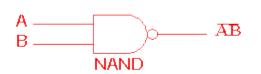


NOIg	jate	
Α	Ā	
0	1	
1	0	

The NOT gate is an electronic circuit that produces an inverted version of the input at its output. It is also known as an *inverter*. If the input variable is A, the inverted output is known as NOT A. This is also shown as A', or A with a bar over the top, as shown at the outputs. The diagrams below show two ways that the NAND logic gate can be configured to produce a NOT gate. It can also be done using NOR logic gates in the same way.



NAND gate



2 Input NAND gate					
Α	В	Ā.B			
0	0	1			
0	1	1			
1	0	1			
1	1	0			



(Deemed to be University)

(Established Under Section 3 of UGC Act. 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

This is a NOT-AND gate which is equal to an AND gate followed by a NOT gate. The outputs of all NAND gates are high if **any** of the inputs are low. The symbol is an AND gate with a small

circle on the output. The small circle represents inversion.

### NOR gate



2 Inpu	2 Input NOR gate						
Α	В	A+B					
0	0	1					
0	1	0					
1	0	0					
1	1	0					

This is a NOT-OR gate which is equal to an OR gate followed by a NOT gate. The outputs of all NOR gates are low if **any** of the inputs are high.

The symbol is an OR gate with a small circle on the output. The small circle represents inversion.

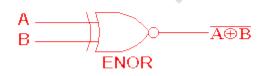
## **EXOR** gate



2 Input EXOR gate							
A B A⊕B							
0	0	0					
0	1	1					
1	0	1					
1	1	0					
-							

The 'Exclusive-OR' gate is a circuit which will give a high output if either, but not both, of its two inputs are high. An encircled plus sign  $(\oplus)$  is used to show the EOR operation.

## **EXNOR** gate



2 Input EXNOR gate						
Α	В	A⊕B				
0	0	1				
0	1	0				
1	0	0				
1	1	1				

The 'Exclusive-NOR' gate circuit does the opposite to the EOR gate. It will give a low output if either, but not both, of its two inputs are high. The symbol is an EXOR gate with a small circle on the output. The small circle represents inversion.



The NAND and NOR gates are called *universal functions* since with either one the AND and OR functions and NOT can be generated.

Note:

A function in *sum of products* form can be implemented using NAND gates by replacing all AND and OR gates by NAND gates.

A function in *product of sums* form can be implemented using NOR gates by replacing all AND and OR gates by NOR gates.

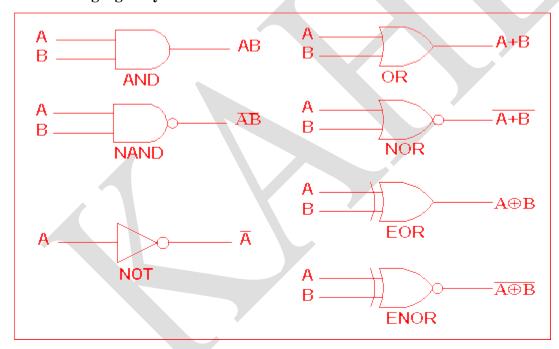


Table 1: Logic gate symbols

Table 2 is a summary truth table of the input/output combinations for the NOT gate together with all possible input/output combinations for the other gate functions. Also note that a <u>truth</u> table with 'n' inputs has  $2^n$  rows. You can compare the outputs of different gates.

### Table 2: Logic gates representation using the Truth table



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

ection 3 of U	GC ACT, 1956	)								
INPUTS					OUTPUTS					
		A	В	AND	NAND	OR	NOR	EXOR	EXNOR	
NOT g	gate	0	0	0	1	0	1	0	1	
Α	Ā	0	1	0	1	1	0	1	0	
0	1	1	0	0	1	1	0	1	0	
1	0	1	1	1	0	1	0	0	1	

Universal Gate | NAND and NOR Gate as Universal Gates

We have discussed different types of logic gates in previous articles. Now coming to the topic of this article we are going to discuss the Universal Gate. AND, NOT and OR gates are the basic gates; we can create any logic gate or any Boolean expression by combining a mixture of these gates.

But NOR gates and NAND gates have the particular property that any one of them can create any logical Boolean expression if appropriately designed. Meaning that you can create any logical Boolean expression using ONLY NOR gates or ONLY NAND gates. Other logical gates do not have this property. If you which to play around with these universal gates as part of an electronics project, many of the best Arduino starter kits contain these universal NOR and

### NAND gates.

Now we will look at the operation of NOR gates and NAND gates as **universal gates**.

### NAND gate as Universal Gate

The below diagram is of a two input NAND gate. The first part is an AND gate and second part is a dot after it represents a NOT gate. So it is clear that during the operation of NAND gate, the



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

inputs are first going through AND gate and after that, the output gets reversed, and we get the final output. Now we will look at the truth table of NAND gate.

We will consider the truth table of the above NAND gate i.e. a two-input gate. The two inputs

are A and Β. А A.B В NAND gate truth table of a nand gate Inputs Output В  $X = \overline{A \cdot B}$ A 0 0 1 0 1

Now we will see how this gate can be used to make other gates.

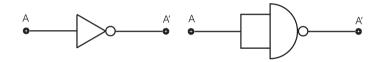
0

1

1

1

0



This is the circuit diagram of a NAND gate used to make work like a NOT gate, the original logic gate diagram of NOT gate is given besides.

1

1

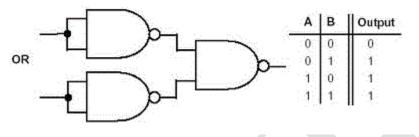


(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

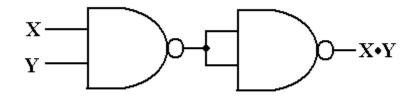
Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com



The above diagram is of an <u>OR gate</u> made from combinations of NAND gates, arranged in a proper manner. The truth table of an OR gate is also given beside the diagram.

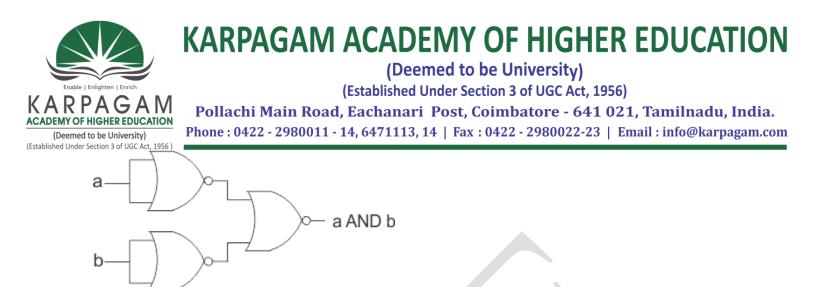
Now we will see the design of an AND gate from NAND gates.



The above diagram is of an AND gate made from NAND gate. So we can see that all the three basic gates can be made by only using NAND gates, that's why this gate is called **Universal Gate**, and it is appropriate.

NOR gate as universal gate

The above diagram is of an OR gate made by only using NOR gates. The output of this gate is exactly similar to that of a single OR gate. We can see the circuit arrangement of OR gate using, NOR gate is similar to that of AND gate using NAND gates.



The above diagram as the name suggests is of AND gate using only NOR gate, again we can see that the circuit diagram of AND gate using only NOR gate is exactly similar to that of OR gate using only NAND gates. Now we will finally see how we can make a NOT gate by using only NOR gates.

The above diagram is of a NOT gate made by using a NOR gate. The circuit diagram is similar to that of NOT gate made by using only NAND gate. So, from the above discussion, it is clear that all the three basic gates (AND, OR, NOT) can be made by only using NOR gate. And thus, it can be aptly termed as **Universal Gate**.

### Laws of Boolean Algebra

As well as the logic symbols "0" and "1" being used to represent a digital input or output, we can also use them as constants for a permanently "Open" or "Closed" circuit or contact respectively.

A set of rules or Laws of Boolean Algebra expressions have been invented to help reduce the number of logic gates needed to perform a particular logic operation resulting in a list of functions or theorems known commonly as the **Laws of Boolean Algebra**.

**Boolean Algebra** is the mathematics we use to analyse digital gates and circuits. We can use these "Laws of Boolean" to both reduce and simplify a complex Boolean expression in an attempt to reduce the number of logic gates required. *Boolean Algebra* is therefore a system of



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

mathematics based on logic that has its own set of rules or laws which are used to define and reduce Boolean expressions.

The variables used in **Boolean Algebra** only have one of two possible values, a logic "0" and a logic "1" but an expression can have an infinite number of variables all labelled individually to represent inputs to the expression, For example, variables A, B, C etc, giving us a logical expression of A + B = C, but each variable can ONLY be a 0 or a 1.

Examples of these individual laws of Boolean, rules and theorems for Boolean Algebra are given in the following table.

## **Truth Tables for the Laws of Boolean**

Boolean Expression	Description	Equivalent Switching Circuit	Boolean Algebra Law or Rule
A + 1 = 1	A in parallel with closed = "CLOSED"		Annulment
A + 0 = A	A in parallel with open = "A"		Identity
A . 1 = A	A in series with closed = "A"		Identity
A . 0 = 0	A in series with open = "OPEN"	~~ ~~	Annulment



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

$\mathbf{A} + \mathbf{A} = \mathbf{A}$	A in parallel with $A = "A"$		Idempotent
A . A = A	A in series with A = "A"		Idempotent
NOT A = A	NOT NOT A (double negative) = "A"		Double Negation
$\mathbf{A} + \mathbf{A} = 1$	A in parallel with NOT A = "CLOSED"		Complement
A . A = 0	A in series with NOT A = "OPEN"	A Ā	Complement
A+B = B+A	A in parallel with $B = B$ in parallel with A		Commutative
A.B = B.A	A in series with B = B in series with A	A B	Commutative
A+B = A.B	invert and replace OR with AND		de Morgan's Theorem
A.B = A + B	invert and replace AND with OR		de Morgan's Theorem

## Laws of Boolean Algebra



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

The basic **Laws of Boolean Algebra** that relate to the *Commutative Law* allowing a change in position for addition and multiplication, the *Associative Law* allowing the removal of brackets for addition and multiplication, as well as the *Distributive Law* allowing the factoring of an expression, are the same as in ordinary algebra.

Each of the *Boolean Laws* above are given with just a single or two variables, but the number of variables defined by a single law is not limited to this as there can be an infinite number of variables as inputs too the expression. These Boolean laws detailed above can be used to prove any given Boolean expression as well as for simplifying complicated digital circuits.

A brief description of the various **Laws of Boolean** are given below with A representing a variable input.

Description of the Laws of Boolean Algebra

A + (B + C) = (A + B) + C = A + B + C (OR Associate Law)

 $A(B.C) = (A.B)C = A \cdot B \cdot C$  (AND Associate Law)

(1) Two separate terms NOR'ed together is the same as the two terms inverted (Complement) and AND'ed for example:  $A+B = A \cdot B$ 

(2) Two separate terms NAND'ed together is the same as the two terms inverted (Complement) and OR'ed for example: A.B = A + B

#### Other algebraic Laws of Boolean not detailed above include:

**Distributive Law** – This law permits the multiplying or factoring out of an expression.

A(B + C) = A.B + A.C (OR Distributive Law) A + (B.C) = (A + B).(A + C) (AND Distributive Law)

<u>Absorptive Law</u> – This law enables a reduction in a complicated expression to a simpler one by absorbing like terms.

A + (A.B) = A (OR Absorption Law)

A(A + B) = A (AND Absorption Law)



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

<sup>CC</sup> Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Associative Law - This law allows the removal of brackets from an expression and regrouping

of the variables.

A + (B + C) = (A + B) + C = A + B + C (OR Associate Law)

 $A(B.C) = (A.B)C = A \cdot B \cdot C$  (AND Associate Law)

#### **Boolean Algebra Functions**

Using the information above, simple 2-input AND, OR and NOT Gates can be represented by 16 possible functions as shown in the following table.

Function	Description	Expression
1.	NULL	0
2.	IDENTITY	1
3.	Input A	A
4.	Input B	В
5.	NOT A	А
6.	NOT B	В
7.	A AND B (AND)	A . B
8.	A AND NOT B	A . B



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

9.	NOT A AND B	A . B
10.	NOT AND (NAND)	A . B
11.	A OR B (OR)	A + B
12.	A OR NOT B	A + B
13.	NOT A OR B	A + B
14.	NOT OR (NOR)	A + B
15.	Exclusive-OR	A . B + A . B
16.	Exclusive-NOR	A . B + A . B

#### Laws of Boolean Algebra Example No1

Using the above laws, simplify the following expression: (A + B)(A + C)

	Q =	(A+B).(A+C)
	A.A + A.C + A.B + B.C	– Distributive law
	A + A.C + A.B + B.C	– Idempotent AND law (A.A = A)
	A(1+C) + A.B + B.C	– Distributive law
	A.1 + A.B + B.C	- Identity OR law $(1 + C = 1)$
	A(1 + B) + B.C	– Distributive law



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.

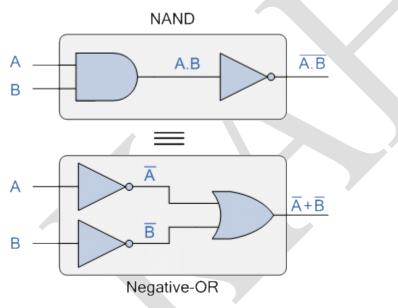
<sup>22</sup> Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

A.1 + B.C	- Identity OR law (1 + B = 1)
A + (B.C)	- Identity AND law (A.1 = A)

Then the expression: (A + B)(A + C) can be simplified to A + (B.C) as in the Distributive law.

#### **DeMorgan's Theorem**

DeMorgan's Theorem and Laws can be used to to find the equivalency of the NAND and NOR gates



As we have seen previously, Boolean Algebra uses a set of laws and rules to define the operation of a digital logic circuit with "0's" and "1's" being used to represent a digital input or output condition. Boolean Algebra uses these zeros and ones to create truth tables and mathematical expressions to define the digital operation of a logic AND, OR and NOT (or inversion) operations as well as ways of expressing other logical operations such as the XOR (Exclusive-OR) function.



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

While George Boole's set of laws and rules allows us to analyise and simplify a digital circuit, there are two laws within his set that are attributed to **Augustus DeMorgan** (a nineteenth century English mathematician) which views the logical NAND and NOR operations as separate NOT AND and NOT OR functions respectively.

But before we look at **DeMorgan's Theory** in more detail, let's remind ourselves of the basic logical operations where A and B are logic (or Boolean) input binary variables, and whose values can only be either "0" or "1" producing four possible input combinations, 00, 01, 10, and 11. Truth Table for Each Logical Operation

Input Variable Output Conditions							
А	В	AND	NAND	OR	NOR		
0	0	0	1	0	1		
0	1	0	1	1	0		
1	0	0	1	1	0		
1	1	1	0	1	0		

The following table gives a list of the common logic functions and their equivalent Boolean notation where a "." (a dot) means an AND operation, a "+" (plus sign) means an OR operation, and the complement or inverse of a variable is indicated by a bar over the variable.





(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

(Deem	ea to be u	(university	
Established Und	ler Section 3	3 of UGC Act,	1956)

AND	A.B
OR	A+B
NOT	А
NAND	A .B
NOR	A+B

DeMorgan's Theory

DeMorgan's Theorems are basically two sets of rules or laws developed from the Boolean expressions for AND, OR and NOT using two input variables, A and B. These two rules or theorems allow the input variables to be negated and converted from one form of a Boolean function into an opposite form.

DeMorgan's first theorem states that two (or more) variables NOR'ed together is the same as the two variables inverted (Complement) and AND'ed, while the second theorem states that two (or more) variables NAND'ed together is the same as the two terms inverted (Complement) and OR'ed. That is replace all the OR operators with AND operators, or all the AND operators with an OR operators.

#### **DeMorgan's First Theorem**

DeMorgan's First theorem proves that when two (or more) input variables are AND'ed and negated, they are equivalent to the OR of the complements of the individual variables. Thus the equivalent of the NAND function and is a negative-OR function proving that  $A \cdot B = A + B$  and we can show this using the following table.

Verifying DeMorgan's First Theorem using Truth Table





(Deemed to be University)

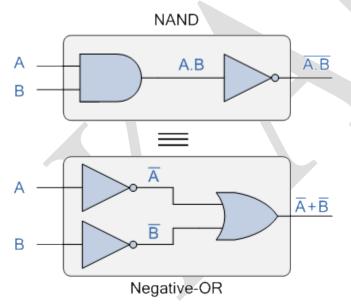
(Established Under Section 3 of UGC Act, 1956)

1	Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India.
	Phone : 0422 - 2980011 - 14, 6471113, 14   Fax : 0422 - 2980022-23   Email : info@karpagam.com

В	A	A.B	A.B	А	В	A + B
0	0	0	1	1	1	1
0	1	0	1	0	1	1
1	0	0	1	1	0	1
1	1	1	0	0	0	0

We can also show that  $A \cdot B = A + B$  using logic gates as shown.

DeMorgan's First Law Implementation using Logic Gates



The top logic gate arrangement of: A.B can be implemented using a NAND gate with inputs A and B. The lower logic gate arrangement first inverts the two inputs producing A and B which become the inputs to the OR gate. Therefore the output from the OR gate becomes: A+B



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

Thus an OR gate with inverters (NOT gates) on each of its inputs is equivalent to a NAND gate function, and an individual NAND gate can be represented in this way as the equivalency of a NAND gate is a negative-OR.

#### **DeMorgan's Second Theorem**

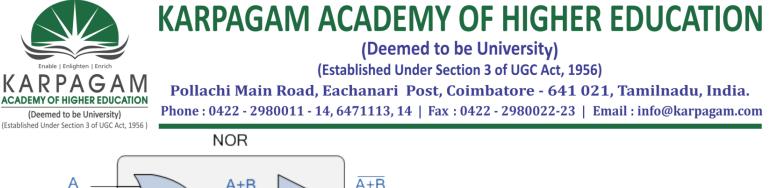
DeMorgan's Second theorem proves that when two (or more) input variables are OR'ed and negated, they are equivalent to the AND of the complements of the individual variables. Thus the equivalent of the NOR function and is a negative-AND function proving that A+B = A.B and again we can show this using the following truth table.

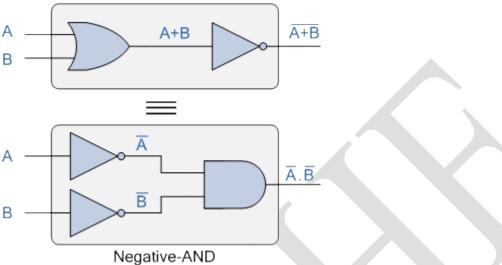
Inputs		Truth Table Outputs For Each Term					
В	А	A+B	A+B	А	В	A . B	
0	0	0	1	1	1	1	
0	1	1	0	0	1	0	
1	0	1	0	1	0	0	
1	1	1	0	0	0	0	

### Verifying DeMorgan's Second Theorem using Truth Table

We can also show that A+B = A.B using logic gates as shown.

DeMorgan's Second Law Implementation using Logic Gates





The top logic gate arrangement of: A+B can be implemented using a NOR gate with inputs A and B. The lower logic gate arrangement first inverts the two inputs producing A and B which become the inputs to the AND gate. Therefore the output from the AND gate becomes: A.B

Thus an AND gate with inverters (NOT gates) on each of its inputs is equivalent to a NOR gate function, and an individual NOR gate can be represented in this way as the equivalency of a NOR gate is a negative-AND.

Although we have used DeMorgan's theorems with only two input variables A and B, they are equally valid for use with three, four or more input variable expressions, for example: For a 3-variable input

A.B.C = A+B+C and also A+B+C = A.B.C For a 4-variable input



(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com

inder Section 3 of UGC Act, 1956 ) A.B.C.D = A+B+C+D

and also

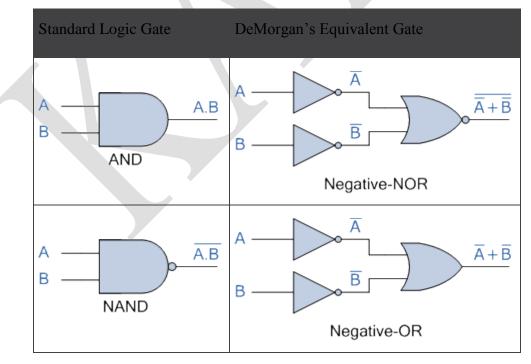
A+B+C+D = A.B.C.D

and so on.

### **DeMorgan's Equivalent Gates**

We have seen here that DeMorgan's Theorems replace all of the AND (.) operators with OR (+) and vice versa and then complements each of the terms or variables in the expression by inverting it, that is 0's to 1's and 1's to 0's before inverting the entire function. Thus to obtain the DeMorgan equivalent for an AND, NAND, OR or NOR gate, we simply add inverters (NOT-gates) to all inputs and outputs and change an AND symbol to an OR symbol or change an OR symbol to an AND symbol as shown in the following table.

### **DeMorgan's Equivalent Gates**

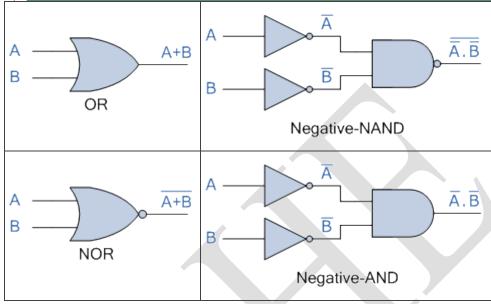




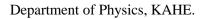
(Deemed to be University)

(Established Under Section 3 of UGC Act, 1956)

Pollachi Main Road, Eachanari Post, Coimbatore - 641 021, Tamilnadu, India. Phone : 0422 - 2980011 - 14, 6471113, 14 | Fax : 0422 - 2980022-23 | Email : info@karpagam.com



Then we have seen that the complement of two (or more) AND'ed input variables is equivalent to the OR of the complements of these variables, and that the complement of two( or more) OR'ed variables is equivalent to the AND of the complements of the variables as defined by *DeMorgan*.



#### [19MMU203] KARPAGAM ACADEMY OF HIGHER EDUCATION, COIMBATORE - 641 021 (Under Section 3 of UGC Act 1956) (For the candidates admitted from 2019 onwards)

#### B. Sc., DEGREE EXAMINATIONS, APRIL - 2020 Second Semester DEPARTMENT OF MATHEMATICS PHYSICS - II

QUESTIONS	OPTION 1	<b>OPTION 2</b>	OPTION 3	<b>OPTION 4</b>	ANSWER
UNIT-I					
If the distance between two charge is doubled the electrostatic force between the charge will be	fourtime more	four time less	will increase into two times	will decrease into two times	four time less
The field due to a wire of uniform charge density at a perpendicular distance y from it	increases with increase in y	decrease with increaase in y	remains constant	depends upon the length of the wire	decrease with increaase in y
Field due to a uniformly charged ring at an axial point at distance very large as compared to the radius of the ring	independent of x	directly proportional to x	directly proportional to x <sup>A</sup>	inversely 2 proportional to x^2	inversely 2 proportional to x^2
Electric charge enclosed by Gaussian surface is	0	1	min	max	0
For gauss's law point charges in closed surface must be distributed	arbitrarily	sequentially	rational	in line	arbitrarily
Electric field intensity outside two charged parallel plate is	$\sigma/2\epsilon_0$	$\sigma  / \epsilon_0$	infinity	0	0
The total electric flux over any closed surface is	ε <sub>0</sub>	$\sigma  / \epsilon_0$	$\epsilon_0 / \sigma$	$q/\epsilon_0$	$q/\epsilon_0$
Electric flux lines due to an infinite sheet of charge is	converging	radial	uniform and perpendicular to the sheet	uniform and parallel to the sheet	uniform and perpendicular to the sheet
One electron volt is	1.6 x 10 <sup>-19</sup> joule	1.6 x 10-19volt	1.6 x 10-19joule	1.6 x 10 -21joule	1.6 x 10 <sup>-19</sup> joule
law establishes a relationship between the electric flux and the electrostatic charge.	Lenz's	Keplers	Faraday	Gauss's	Gauss's
The ratio $\epsilon/\epsilon 0$ is a dimensionless quantity known as	relative permeability	relative permittivity	absolute permittivity	permeability	relative permittivity
The electric field lines begin at the charge and terminate at the charge.	positive, negative	negative, positive	both positive	both negative	positive, negative
Total electric flux emanating from a charge q coulomb placed in air is	$q/\epsilon_0$	$\varepsilon_0 \; q$	q	4πq	$q/\varepsilon_0$
Gauss's law due to different charge distribution is used to calculate	electric field	electric charge	electric intensity	electric field lines.	electric intensity
The total flux across a closed surface enclosing charge is independent of	shape of the closed surface		volume of the enclosure	charges within the surface	all

The unit of Electric flux is	Gauss's	Weber	Nm <sup>-2</sup> c <sup>-1</sup>	Nc <sup>-1</sup>	$\mathrm{Nm}^{-2}\mathrm{c}^{-1}$
.Mechanical pressure on the surface of a charged conductor having surface charge density $\sigma$ is	$\varepsilon_0\sigma^2$	$\sigma^2/\varepsilon_0$	$\sigma^2/2\varepsilon_0$	$\sigma/2\epsilon_0$	$\sigma^2/2\varepsilon_0$
If the separation between two charges is increased the electric potential energy	always decreases	always increases	remains the same	may increase or decrease	may increase or decrease
Electric intensity due to an infinitely long plane sheet of a conductor at appoint close to its surface is	independent of r	proportional to $1/r^2$	proportional to r	inversely proportional to 1/r	independent of r
The total electric flux through a closed surface depends on	location of the charge only	the shape of the closed surface only	the value of the net charge only	both charge and shape	the value of the net charge only
Electric field intensity due to an infinite plane sheet of charge is	$\sigma/\epsilon_0$	$q/2\epsilon_0$	$\sigma/2\epsilon_0$	$q/\epsilon_0$	$\sigma/2\epsilon_0$
Law stated as flux is ${}^{1}\!/_{Eo}$ times total charge is	ohms law	newton's law	gauss's law	coulombs law. dependent on the	gauss's law
A Gaussian sphere closes an electric dipole within it. Then the total flux through the sphere is	half due to a single charge	double due to a single charge	zero	position of the dipole	zero
The Flux of electric field is	scalar	vector	zero	infinity	scalar
Flux density is measured in	Tesla	Weber	Ampere- turn	Maxwell	Tesla
Which of the following quantities are scalar?	dipole moment	electric force	electric field	electric potential	electric potential
A dipole is placed in a uniform electric field with its axis parallel to th field. It experiences	e only a net force	only a torque	both a net force and torque	e neither a net force nor a torque	e neither a net force nor a torque
Electric potential energy U of two point charges is	$q_1 q_2 / 4 \epsilon_0 \pi r^2$	$q_1 q_2 / 4 \epsilon_0 \pi r$	pEsinθ	pEcosθ	$q_1 q_2 / 4 \epsilon_0 \pi r$
If a point lies at a distance x from the midpoint of the dipole, the electric potential at the point is proportional to	c 1/x <sup>2</sup>	1/x <sup>3</sup>	1/x <sup>4</sup>	1/x <sup>3/2</sup>	1/x <sup>2</sup>
The law that governs the force between electric charge is called	Amperes	Coulomb law	Faraday	Ohms	Coulomb law
The minimum value of the charge in any object cannot be less than	1.6 x 10 <sup>-19coulomb</sup>	3.2 x 10 <sup>-19 Coulomb</sup>	4.8 x 10 <sup>-19Coulomb</sup>	1 coulomb	1.6 x 10 <sup>-19coulomb</sup>
An electric field can deflect	X rays	neutrons	alpha particle	gamma rays	alpha particle
Inside the hollow spherical conductor, the potential	is constant	varies directly as the distance from the centre	varies inversely as the distance from the centre	the square of the distance from the centre	is constant
The intensity at a point due to a charge is inversely proportional to	amount of the charge	size of the charge	distance of the point	square of the distance from the charge	square of the distance from the charge
The distance between two charge is douled then the force between them would become	half	one-fourth	doubled	four times	one-fourth
A surface enclosed an electric dipole, the flux through the surface is	infinite	positive	negative	zero	zero

Electric potential is a	vector quantity	scalar quantity	neither vector nor scalar smaller than the	fictitious quantity greater than the	scalar quantity
The potential at any point inside a charged sphere is	zero	same as potential on the surface	potential on the surface	potential on the surface	same as potential on the surface
Two smallspheres eaach carrying a charge q are placed r metre apart, one of the spheres is taken around the other one in a circular path, the work done will be equal to	force between them x r	force between them x 2r	force between them / 2nr	zero	zero
State which of the following is correct?	J=Coulomb x volt	J=Coulomb / volt	J= volt/ ampere	J= volt x ampere	J=Coulomb x volt
A positively charged glass rod attracts an object. The object must be	negatively charged	either negative charged or neutral	neutral	positively charged	either negative charged or neutral
A charge q islocated at the centre of a hypothetical cube. The electric flux through any face of the cube is	q/є0	q/2є0	q/4є0	q/4є0	q/4€0
The force between two electrons seperated by a distance r varies as	r^2	relative permittivity	r^-1	r^-2	r^-2
The energy stored per unit volume of the medium of relative permittivity is	$\varepsilon_r \varepsilon_0 E^2/2$	$\epsilon_0 E^2/2$	$\varepsilon_{\rm r}\varepsilon_0 E/2$	$\epsilon_0 E/2$	$\varepsilon_r\varepsilon_0 E^2/2$
All magnetic moments within a domain will point in the direction.	Different	Same	Positive	Negative	Same
The electrical energy consumed by a coil is stored in the form of:	magnetic field	force field	electrostatic field	electrical field	magnetic field
Electricity may be generated by a wire:	carrying current	wrapped as a coil	passing through a flux field	that has neutral domains	passing through a flux field
A magnetic field has:	lines of reluctance	polar fields	lines of force	magnetomotive force	lines of force
The polarity of induced voltage while a field is collapsing is	opposite to the force creating it	independent of the force creating it	field	present only if the force is stationary	
What is magnetic flux?	the number of lines of force in webers	s of force in	the number of lines of force in flux density		s the number of lines of force in webers
The energy stored in the charged capacitor is	1/2 CV <sup>2</sup>	1/2qV	v/q	qV	1/2 CV <sup>2</sup>
The arrangement in which one conductor is charged and other is earthed i named as	s capacitor	condenser	capacitor/ condneser	comparator	capacitor/ condneser
device is useful to reduce voltage fluctuations in electric power supplies.	capacitor	condenser	converter	comparator	capacitor
The capacitance of a capacitor C is	q/v	qv	v/q	v/q	q/v
capacitors can bewidely used in the tuning circuits of radio receivers.	mica	electrolytic	paper	variable air	variable air
capacitors are used widely in a radia-set as a smoothing capacitors	electrolytic	mica	both mica and electrolytic	variable	both mica and electrolytic
capacitor is used in a.c bridges	electrolytic	variable air capacitor	both mica and electrolytic	mica	variable air capacitor

device is used to measure electrostatic potentials A dielectric slab is introduced between the plates of an isolated charged	electrometers	magnetometers	potentiometer	galvanometer	electrometers
parallel plate air capacitor. Which of the following quantities will remain unchanged?	charge on the capacitor	p.d across the capacitor	energy of the capacitor	electric field inside the capacitor	charge on the capacitor
The p.d across a capacitor is kept constant.If a dielectric slab of dielectric constant K is introduced between the plates, the stored energy will be	decreases by a factor K	increases by a factor K	remains constant	decreases depending on the nature of the	increases by a factor K
Capacitance has the dimension	$M^{-1}L^{-2}T^{-4}I^{2}$	$ML^{2}T^{-4}I^{-2}$	$MLT^{-3}I^{-1}$	$M^{-1}L^{-1}T^3I$	$M^{-1}L^{-2}T^{-4}I^{2}$
In gauss's law the electric flux E through a closed surface (s) depends on the value of net charge	Inside the surface	outside the surface	on the surface	in the surface	Inside the surface
The unit of capacitance is	Farad	coulomb/volt	Farad and Coulomb/Volt	ohm	Farad and Coulomb/Volt
device is used to generate and detect electromagnetic oscillation of high frequency.	capacitor	voltmeter	Resistor	galvanometer	capacitor
The normal component of D are across the boundary by the surface charge density	continuous	Discontinuous	Random	Discrete	Discontinuous
The tangential component of the electric field is across the boundary.	continuous	Discontinuous	Random	Discrete	continuous
The potential difference between the conductors is proportional to the on the capacitor.	charge	voltage	current	time	charge
The coaxial cable used in communication system is a common example for which type of capacitor	spherical	cylindrical	Air capacitor	Parallel plate capacitor	Parallel plate capacitor
Variable air capacitor is used in	A.c bridges	D.c bridges	both a and b	tuning circuits	A.c bridges
capacitors can be used only in unidirectional power supplies.	mica	Electrolytic	paper	variable	Electrolytic
An electron- volt (eV) is a unit of	Energy	Potential difference	Charge	Momentum	Energy
Farad is the unit of	capacitance	self inductance	mutual inductance	conductance of an electrolyte	capacitance
In a charged capacitor the energy is stored in	the field between the plates	positive chaarge	negative charge	neutral	the field between the plates
No current flows between two charged bodies connected together when they have the same	charge	potential	capacitance	resistance	potential
Dielectric materials are	insulating materials	semiconducting materials	magnetic materials	ferroelectric materials	insulating materials
Dielectric constant of vacuum is	infinity	100	) one	zero	one
For making a capacitor it is better to select a dielectic having	low permittivity	high permitivitty	permitivity same as that of air	permitivity slightly more thanair	high permitivitty
A dielectric material must be a	resistor	insulator	good conductor	semi conductor	insulator

Which of the following material has highest value of dielectric constant	glass	vacuum	ceramics	oil	ceramics
When a dielectric slab is introduced in a parallel plate capacitor, the potential difference between the plates will	remain unchanged	decrease	increases	becomes zero	decrease
A capacitor consists of	two insulators seperated by a condenser	two conductors seperated by an insulator	2 insulator	2 conductor	two conductors seperated by an insulator

#### Unit-II

Which of the following material requires least magnetizing field to magnetize it?	Gold	Silver	Tungsten	Cobalt	Cobalt
Basic source of magnetism	Charged particles alone	Movement of charged particles	Magnetic dipoles	Magnetic domains	Movement of charged particles
Units for magnetic flux density	Wb / m^2	Wb / A.m	A/m	Tesla / m	Wb / m^2
Magnetic permeability has units as	Wb / m^2	Wb / A.m	A/m	Tesla / m	Wb / A.m
Magnetic field strength's units are	Wb / m^2	Wb / A.m	A/m	Tesla / m	A/m
Example for dia-magnetic materials	super conductors	alkali metals	transition metals	Ferrites	super conductors
Example for ferro-magnetic materials	super conductors	alkali metals	transition metals	Ferrites	alkali metals
Example for anti-ferro-magnetic materials	salts of transition elements	rare earth elements	transition metals	Ferrites	rare earth elements
Example for ferri-magnetic materials	salts of transition elements	rare earth elements	transition metals	Ferrites	transition metals
Magnetic susceptibility para-magnetic materials is	0.00001	-10^-5	10^5	10^-5 to 10^-2	0.00001
Magnetic susceptibility dia-magnetic materials is	0.00001	-10^-5	10^5	10^-5 to 10^-2	10^-5 to 10^-2
Magnetic susceptibility ferro-magnetic materials is	0.00001	-10^-5	10^5	10^-5 to 10^-2	10^-5 to 10^-2
Typical size of magnetic domains (mm)	1 to 10	0.1-1	0.03	5 0.001	0.001
Example for magnetic material used in data storage devices	45 Permalloy	CrO2	Cunife	Alnico	CrO2
Susceptibility of a magnetic material is defined as the ratio of	Intensity of magnetization	magnetizing field	magntic induction	Conductivity	Intensity of magnetization
Those substances which are weakly magnetized in the direction opposite to that of the external magnetic field are called as	ferromagnetic substances	diamagnetic	paramagnet	anti ferro	diamagnetic

The magnetic field that exists in vacuum and induces magnetism is called The temperature at which the domain structure gets destroyed and	magnetic intensity	magnetizing field	magnetic field intensity	magnetization	magnetizing field
ferromagnetic substance is converted into paramagnetic substance is called as	zeropoint temp.	high temperature	curie temperature	domain thoery	curie temperature
The ability of magnetizing field to magnetize a material medium is called	magnetic intensity	magnetic field	magnetic field intensity	magnetization	magnetic field intensity
Relative Magnetic Permeability (µr) =	1+χ	1/χ	1+χ	1+H	1+χ
Those substances which are strongly magnetized in the direction of the external magnetic field are called as	paramagnet	anti ferro	diamagnetic	ferromagnetic substances	ferromagnetic substances
Magnetic fields do no interact with	Moving permanent magnets	Stationary permanent magnets	Moving electric charges	Stationary electric charges	Stationary electric charges
The magnetic permeability of a material is defined as ratio of magnetic induction (B) to	susceptibility	magnetic intensity	magnetic field intensity	magnetic field	magnetic intensity
Above Curie temperature, Ferromagnetic substances become	magnet	anti ferro	diamagnetic	paramgnetic	paramgnetic
is defined as pole strength per unit area of cross section of material	susceptibility	magnetizing field	magnetic field intensity	intensity of magnetization	intensity of magnetization
The Curie Temperature (°K ) for Nickel is °K	136	5 316	613	631	631
The degree or extent to which magnetic field can penetrate or permeate a material is called	magnetic permeability	susceptibility	magntic induction	intensity	magnetic permeability
Total number of magnetic lines of force crossing per unit area normally through a material is called	magnetic permeability	susceptibility	magntic induction	-	magntic induction
The magnetic energy stored in an inductor is current	Directly proportional to	Inversely proportional to	Directly proportional to the square of	Inversely proportional to the square of	Directly proportional to the square of
The ratio of the permeability of material to the permiability of air or vacuum.	Relative permeability	Relative permittivity	Relative conductivity	Relative reluctivity	Relative permeability
The property of magnetic materials of retaining magnetism after withdrawal of the magnetizing force is known as	Retentivity	Reluctivity	Resistivity	Conductivity	Retentivity
The force between two magnetic poles varies with the distance between them. The variation is to the square of that distance.	Equal	Greater than	Directly proportional	Inversely proportional	Inversely proportional
Permeability means	of the material for magnetic lines of force	test in the material after exciting field has beenremoved	the strength of an electromagnet	The strength of the permanent magnet	-
The magnetic field inside a solenoid	is constant	is uniform	increases with distance from the axis	decreases with distance from the axis	is uniform
Paramagnetic substance has a relative permeability of	Slightly less than one	Equal to one	Slightly greater than one	Very much greater than one	Slightly greater than one
For which of the following substance, the magnetic susceptibility is independent of temperature	Dia	Para	Ferro	ferri	Dia
—	high susceptibility	high susceptibility and high	low susceptibility	low susceptibility and high	high susceptibility

Magnetic field is always	solenoidal	Irrotational	harmonic in character	rotational	solenoidal
Magnetic flux has the dimension	$ML^{2}T^{-2}I^{-1}$	MLT <sup>-2</sup> I <sup>-1</sup>	ML <sup>2</sup> TT <sup>1</sup>	ML <sup>2</sup> T <sup>-2</sup> I	$ML^{2}T^{2}I^{1}$
The permeability of free space is WbA-1m-1.	$\mu_0 = 4\pi$	$\mu_0 = 4\pi x 107$	$\mu_0 = 4\pi x 10^{-7}$	μ <sub>0</sub> =4πx10 -8	$\mu_0 = 4\pi x 10^{-7}$
The intrinsic magnetic moment of the atoms of a material is not zero. The material	must be paramagnetic	must be diamagnetic	must be ferromagnetic	may be paramagnetic or ferromagnetic	may be paramagnetic or ferromagnetic
The relative permeability of a material is 0.98. The material must be	paramagnetic	diamagnetic	ferromagnetic	ferrimagnetic	diamagnetic
Hysteresis refers to the between flux density of the material and the magnetizing force applied.	Leading effect	Ratio	Equality	Lagging effect	Lagging effect
Hydrogen is an example of a material.	Paramagnetic	Diamagnetic	Ferromagnetic	Non- magnetic	Paramagnetic
Cobalt is an example of a material	Paramagnetic	Diamagnetic	Ferromagnetic	Non- magnetic	Ferromagnetic
Magnetic intensity is a	Phasor quantity	Physical quantity	Scalar quantity	Vector quantity	Vector quantity
The core of a magnetic equipment uses a magnetic material with	Least permeability	Low permeability	Moderate permeability	High permeability	High permeability
Which of the following is a paramagnetic material?	Carbon	Copper	Bismuth	Oxygen	Oxygen
crystals are frequently used in computers memory cells.	Ferroelectric	Diamagnetic	Paramagnetic	Ferrielectric	Ferroelectric
Platinum exhibits the property of	diamagnet	ferromagnet	paramagnet	none	paramagnet
Diamagnetic substance are attracted by magnetic field. The attraction is	very strong	weak	zero	negative	weak
A moving charge produces	electric field only	magnetic field only	both electric and magnetic fields	neither electric nor magnetic	both electric and magnetic fields
Differential form of Ampere's law for a steady current is	$\Delta xH=J+\partial D/\partial t$	$\Delta x B = \mu_0 J$	Δ.B=0	$\int B.dl = \mu 0I$	$\Delta x B = \mu_0 J$
The phenomenon by which a magnetic substance becomes a magnet when it is place near a magnet	Magnetic effect	Magnetic phenomenon	Magnetic induction	Electromagnetic induction	Electromagnetic induction
Which of the following materials has permeability slightly less than that o free space?	f Paramagnetic	Non- magnetic	Ferromagnetic	Diamagnetic	Diamagnetic
The property of a material which opposes the creation of magnetic flux in it	Resistance	Reluctance	Permeance	Conductance	Reluctance
The susceptibility of a paramagnetic gas varies as the absolute temperature.	directly	inversely	Similarily	opposite	inversely
If the relative permeability is less than 1,then the material will be	dia	para	ferro	ferri	dia

If the relative permeability is greater than 1, then the material will be	dia	para	ferro	ferri	para
If the relative permeability is very much greater than 1, then the material will be	dia	para	ferro	ferri	ferro
Lenz's law is a consequence of the law of conservation of	energy	momentum	mass	charge	energy
Lenz's law does not violate the principle of	conservation of charge	conservation of energy	conservation of mass conservation of momentum		conservation of energy
The direction of the induced emf during electromagnetic induction is determined by	Lenz's law	Amperes law	Maxwell law	Faaradays law	Lenz's law
Alternative current generator is basically based upon	amperes law	Lenz's law	faradays law	coulombs law	faradays law
Moving a coil in and out of magnetic field induces	force	potential difference	emf	voltage	emf
Which two values are plotted on a B-H curve graph?	reluctance and flux density	permeability and reluctance	magnetizing force and permeability	flux density and magnetizing force	flux density and magnetizing force
	needon of the			unection of an	
Faraday's law states that the:	induced voltage produces an opposition	emf is related to the direction of the current	emf depends on the rate of cutting flux	induced current produces an aiding effect	emf depends on the rate of cutting flux
Faraday's law states that the: What does Faraday's law concern?	induced voltage produces an	the direction of the current	the rate of cutting flux a magnetic field in	induced current produces an aiding effect	the rate of cutting
	induced voltage produces an opposition a magnetic field in	the direction of the current a magnetic field cutting a conductor lag between cause	the rate of cutting flux a magnetic field in	induced current produces an aiding effect a magnetic field hystersis lead between cause	the rate of cutting flux a magnetic field cutting a conductor

#### UNIT - III

UNIT - III					
	Newtons		Maxwell's		
	corpuscular theory	Huygen's wave	electromagnetic	Planck's quantum	Planck's quantum
Einstein's theory of photoelectric effect is based on	of light	theory of light	theory of light	theory of light	theory of light
The equation $E=hv$ was deduced by:	Heisenberg	de Broglie	Einstein	Planck	Einstein
De Broglie wavelength ( $\lambda$ ) associated with moving particles, mass, m, and					
velocity v is	h/mv	h/√2mEk	h/√2mqV	h/√2mkT	h/mv
Based on quantum theory of light, the bundles of energy =	hv	hλ	h/v	h/λ	hv
De Broglie wavelength ( $\lambda$ ) associated with moving particles of K.E is	h/mv	h/√2mEk	h/√2mqV	h/√2mkT	h/mv
Wave nature is not observed in daily life because we are using	Microscopic	macroscopic			macroscopic
	particles	particles	molecules	atoms	particles
	1001	1000	1000	10.95	1000
year de Broglie proposed that the idea of dual nature.	1921	1922	1923	1925	1923
de Broglie wavelength ( $\lambda$ ) associated with charge q and potential	• /	1 / 6 . 51	1.10	1/10.17	• /
difference of V volts is	h/mv	h/√2mEk	h/√2mqV	h/√2mkT	h/mv
				an electron	
The photoelectric effect was explained by Albert Einstein by assuming			an electron	behaves as a	
that:	light is a wave.	light is a particle.		particle.	light is a particle
	Special Theory of		Thomson model of		
The Compton Effect supports which of the following theories?	Relativity.	Light is a wave.	the atom.	Light is a particle.	Light is a particle.
Which one of the following objects, moving at the same speed, has the					
greatest de Broglie wavelength?	Neutron	Electron	Tennis ball	Bowling ball	electron
Which of the following formulas can be used to determine the de Broglie					
wavelength?	$\lambda = hmv$	$\lambda = h/mv$	$\lambda = mv/h$	$\lambda = hm/c$	$\lambda = h/mv$
The idea of dual nature of light was proposed by	Plank	De Broglie	Einstein	Maxwell	De Broglie

According to the de Broglie's hypothesis of matter waves, the concepts of energy, momentum and wavelength are applicable to		moving particles as well as to radiation (photon) Davisson and Germer	but not to moving particles	neither to moving particles nor to radiation (photon).	moving particles as well as to radiation (photon) Davisson and Germer
Experimental verification of de Broglie's matter waves was obtained in	experiment	Experiment	Compton"s Experiment	Plank Davisson and	Experiment Davisson and
The first experimental evidence for matter waves was given by The de Broglie wavelength wave length of a moving electron subjected to	Einstein	de Broglie	Plancks	Germer	Germer
a potential V is Compute the de Broglie wavelength of an electron that has been accelerated through a potential difference of 9.0 kV. Ignore relativists	1.26/V1/2	12.26/V1/2	12.26/V	2.26/V1/2	12.26/V1/2
effects.	1.3 x 10-11 m	1.7 x 10 <sup>-22</sup> m	1.2 x 10 <sup>-26</sup> m	1.7 x 10 3 m	1.7 x 10 <sup>-22</sup> m
Heisenberg's uncertainty principle states for the energy and time is	$\Delta E \Delta t = h$	$\Delta E \Delta t = h/2\pi$	$\Delta E \Delta t = 2\pi h$ Triply ionized	$\Delta E\Delta t = 2 \pi /h$ Doubly ionized	$\Delta E \Delta t = h/2\pi$
In which of the following is the radius of the first orbit minimum?	hydrogen atom	A tritium atom	beryllium	helium	hydrogen atom
The Kinetic energy of electron of mass (m) is given by (T) Heisenberg's uncertainty principle states for the angular momentum and	p/2m	p²/2m	2mp	2mp <sup>2</sup>	p²/2m
angle is	$\Delta J \Delta \Theta = h$	$\Delta J \Delta \Theta = h/2\pi$	$\Delta J \Delta \Theta = 2\pi h$	$\Delta J \Delta \Theta = 2 \pi / h$	$\Delta J \Delta \Theta = h/2\pi$
The radius of the nucleus of any atom is of the order of m Heisenberg's uncertainty principle states for the position and momentum	10-8 m	10 -14 cm	10-14m	10-10 m	10-14m
is The uncertainty in the total energy ( $\Delta E$ ) is	$\Delta p \Delta q = h$ $\Delta T + \Delta V$	$\Delta p \Delta q = h/2\pi$ $\Delta T - \Delta V$	$\Delta p \Delta q = 2\pi h$ $\Delta T$	$\Delta p \Delta q = 2 \pi / h$ $\Delta V$	$\Delta p \Delta q = h/2\pi$ $\Delta T + \Delta V$
Based on the uncertainty principle, the minimum momentum (Pmin) =	h/I	ħ	ħl	1/ ħ	ħ
Who proposed the uncertainty principle?	Bohr	De Broglie	Heisenberg	Schroedinger	Heisenberg
The kinetic energy of electron in the atoms is	4 Mev	6 Mev	8 MeV	97 Mev	97 Mev
According to Heisenberg"s Uncertainty principle, Indeterminism in the measurement of canonically conjugate variables is due to	imperfection in measuring instruments	imperfection in measurement methods		n inherent in the world itself	imperfection in measuring instruments
The value of h is	6.625 x 10 <sup>-34</sup> nm	5 x 10 <sup>-34</sup> nm	1.055 x 10 <sup>34</sup> nm	1.0555 x 10 <sup>-34</sup> nm	1.055 x 10 <sup>34</sup> nm
The mass of an electron is	9 x 10 <sup>-34</sup> nm	$9 \times 10^{-31} \text{ m}$	$6 \ge 10^{-34} \text{ nm}$	6.625 x 10 <sup>-30</sup> nm	9x 10 <sup>-31</sup> m
If we measure the position of a particle accurately then the uncertainty in measurement of momentum at the same instant becomes	0	Infinity	1	constant	Infinity
If we measure the energy of a particle accurately then the uncertainty in measurement of the time becomes	0	Infinity	1	constant	Infinity
	macroscopic	microscopic			microscopic
Uncertainty principle is applicable to	particles	particles	gases	liquids photoelectric	particles
Uncertainty principle can be easily understandable with help of Heisenberg gave his concept in	Dalton's effect 1923	Compton's effect 1927 information	electron effect 1957 data	effect 1933	Compton's effect 1927 information
Heisenberg uncertainty principle is used for	data processing	processing	processinerosion	dilation	processing
The Heisenberg uncertainty principle is concerned with what two		momentum and	position and	momentum and	momentum and
properties?	mass and velocity	position	velocity	mass	position
Energy of photon is directly related to the forms of Schroedinger's equation describe the motion of non-	wavelength	wave number	frequency	amplitude	frequency
relativistic material particle. If $\psi_1$ and $\psi_2$ are two different wave functions, both being satisfactory	$H\psi = E\psi$	$H\psi\neq E\psi$	$H\psi \le E\psi$	$H\psi > E\psi$	$H\psi = E\psi$
solution of wave equation for a given system, then these functions will be normalized, if	$\psi_j {}^{*} \psi_j d\tau = 1$	$\psi_j{}^{*}\psi_j d\tau \neq 1$	$\psi_j {}^{\ast} \psi_j d\tau \geq 1$	$\psi_j {}^{ \ast} \psi_j d\tau \leq 1$	$\psi_j {}^{*} \psi_j d\tau = 1$
Schrodinger suggested seeking solutions of the waves equation which represents waves.	non-progressive	progressive	non-standing	standing	standing
Momentum operator in Schroedinger equation (Pop) is	ħ/i	ħi	i/ħ	H	ħ/i
The minimum energy of a particle in a box (E) is	$\hbar^2/ml^2$	$\hbar^2/2ml^2$	$ml^2/\hbar^2$	$2ml^2/\hbar^2$	$\hbar^2/2ml^2$
The Schroedinger time-dependent wave equation is	$H\psi=E\psi$	$H\psi\neq E\psi$	$H\psi \leq E\psi$	$H\psi \geq E\psi$	$H\psi=E\psi$
The time-dependent Schroedinger equation is partial differential equation	1	2	3	4	3
having variables.	-	$\Delta^2 \psi + (2m/\hbar^2)(E)\psi$			
The Schroedinger equation for a free particle is	$\Delta \psi + (2m/h)(E)\psi$ = 0	$\Delta \psi + (2m/h)(E)\psi \neq 0$	$\Delta \psi + (2m/n)(E)\psi$ < 0	$\Delta \psi + (2m/h)(E)\psi$ > 0	$\Delta \psi + (2m/h)(E)\psi = 0$
The time independent form of Eop is	Н	v	U	Т	Н
			an imaginary		
Wave function $\Psi$ of a particle is Wave function is represented by	real quantity Ψ	a complex quantity E	quantity H	any one of these W	real quantity Ψ
Schroedinger attempt the physical interpretation of $\psi$ in terms of	volume density	current density	density	charge density	charge density
In wave function, energy per unit volume is equal to	$A^2$	E <sup>2</sup>	H2	$\psi^2$	A <sup>2</sup>
Photon density is	hv	A <sup>2</sup> /h	$A^2/v$	A <sup>2</sup> /hv	A <sup>2</sup> /hv
Photon density is proportional to	hν	$A^2$	h	ν	$A^2$
Particle density is proportional to	hv	$\psi^2$	h	ν	$\psi^2$
Schrodinger's equation described the	wave function	complement of the wave function	behavior of "matter" waves	motion of light	behavior of "matter" waves

Solutions to Schrodinger's equation are labeled with	psi	phi	mu	pi	psi
The hypothesis that nuclear forces possess an exchange character was put					
forward by	Pauli	Rutherford	Heisenberg	Max Plank	Heisenberg
Heisenberg force is due to UNIT - IV	exchange of space	exchange of spin	exchange of space a	exchange of mome	r exchange of space a
The potential involved outside the nucleus is	gravitational	electromagnetic	nuclear	Coulombic	Coulombic
	the mass of the	the mass of the	the mass of the	the mass of the	the mass of the
The atomic mass is almost equal to	electron	nucleus	protons	neutrons	nucleus
The nuclear radius is proportional to	A^2/3	А	A^1/3	A^2	A^1/3
The nucleon density at the centre of any nucleus is	proportional to A elelctromagnetic	proportional A^2	proportional Z strong nuclear	almost the same	almost the same strong nuclear
The force which holds the nucleons together in a nucleus is	force elelctromagnetic	gravitational force	force	weak interaction	force
The non-central part of the nuclear force is called	force	tensor force	magnetic force	static force	tensor force
	exchange of			exxchange of	exchange of
Nuclear exchange forces arise due to	mesons	exchange of charge	exchange moments	strangeness charge keeps on	mesons
Nucleus is	positively charged	negatively charged	neutral	changing	positively charged
	1637 times of an	1737 times of an	1837 times of an	1937 times of an	1837 times of an
Proton has the charge	electron	electron	electron	electron	electron
	1639 times of an	1739 times of an	1839 times of an	1939 times of an	1839 times of an
Neutrons has the charge	electron	electron	electron	electron	electron
The difference between the total mass of the individual nucleons and the					
mass of the nucleus is known as	mass defect	binding energy	packing fraction	mass excess	mass defect
The mass of the nucleus is normally the total mass of the nucleons	greater than	equal to	less than	can be anything	less than
Instrument used to measure nuclear masses and their other properties is called	Mass spectrograph	nuclear spectrometer	NMR spectrometer	magnetic spectrometer	Mass spectrograph
	liquid drop model				liquid drop model
The constant nucleon density inside the nucleus supports	of the nucleus	shell model	collective model	unified model	of the nucleus
The constant binding energy per nucleon supports	shell model	collective model	liquid drop model	unified model	liquid drop model
In the liquid drop model, the restoring force after deformation is supplied		gravitational			
by	internal force	attraction	surface tension	repulsion	surface tension
The surface energy is proportional to where A is the mass number	А	A^1/3	A^2/3	A^2	A^2/3
	surface vibration	surface energy of		low lying discrete energy levels of	low lying discrete energy levels of
The liquid drop model could not explain satisfactorily	of the nuclei	the nuclei	all the above	nuclei	nuclei
The fiquid drop filoder could not explain satisfactority	a sphere of	poly-atomic	an the above	poly-atomic	poly-atomic
	individual	molecule of alpha	alpha and beta	molecule of beta	molecule of alpha
According to alpha particle model, a nucleus can be considered as	nucleons	particles	particles	particles	particles
According to alpha particle model, a nucleus can be considered as	nucleons	particles	particles	particles	particles
	nuclei other than				nuclei other than
Alpha particle model could not describe the ground and excited states of	even-even nuclides	even-even nuclides	even-odd nuclides	odd-even nuclides	even-even nuclides
It is seen that nuclei with nucleons are most stable, where n=1,2,3,	2n-1	4n-2	4n	2n	4n
The nuclei with $Z =$ and are found to be more than usually					
stable	50, 20	50,40	20, 40	30, 40	50, 20
The resemblance of the nucleus with a drop of liquid led to the suggestion					
of model.	Fermi gas model	collective model	liquid drop model	Shell model	liquid drop model

There are ---- types of beta emission The spin of the beta particle is 1-Feb 0 1-Feb 3-Feb 1 positron What is the most penetrating radiation? beta gamma alpha gamma they are equally Which types of radiation is the most dangerous? gamma alpha beta dangerous gamma A particle striking on the target nucleus, is absorbed by it and a new particle photo disintegration radiative capture elastic scattering disintegration disintegration Emission of alpha and beta rays is an example of photo disintegration radiative capture spontaneous decay spallation reaction spontaneous decay The strong nuclear force is charge dependent both charge and size charge indendent size dependent charge indendent

shell model

radiative capture

Collective model

three

1

no parity

large

2

outside the nucleus inside the nucleus

liquid drop model

fusion

Unified model

two

1-Feb

odd

small

1

Fermi gas model

gamma ray

emission

optical model

seven

from the external

orbits

3-Feb

even

medium

3

collective model

fission

Super-conductivity

model

four

inside a proton

0

odd or even

zero

4

liquid drop model

radiative capture

Collective model

four

inside the nucleus

0

even

small

3

The nuclear fission can be best explained using

the critical energy, ----- takes place.

groups, known as islands of isomerism

The penetrating power of alpha particle is

Alpha particle is emitted from

The spin of an alpha particle is

Alpha particle is of ----- parity

As per liquid drop model, if the energy of the incident neutron is less than

Nuclei with N or Z near the end of a shell are found in ...... Distinct

Which model is the combination of liquid drop and shell model

UNIT-V					
Which number system is not a positional notation system?	ROMAN	Binary	decimal	Hexadecimal	ROMAN
The 10's complement of the octal number 715 is	63	539	285	395	539
The 9's complement of 381 is	372	508	618		618
The 1's complement of the binary number 1101101 is	10	100010	10011	1101110	100010
The 2's complement of the binary number 010111.1100 is	101001.11	101000.01	10111.0011	101000.0011	101000.01
Which system has a base or radix of 10:	Binary digit	Hexadecimal digit	Decimal digit	Octal digit	Decimal digit
In which counting, single digit are used for none and one:	Decimal counting	Octal counting	Hexadecimal counti	Binary counting	Binary counting
In which numeral every position has a value 2 times the value f the positi		Octal	Hexadecimal		Binary
In which digit the value increases in power of two starting with 0 to left of		Decimal	Binary		Binary
Which system is used in digital computers because all electrical and electric			Octal number Decimal		Binary number
Which number is formed from a binary number by grouping bits in groups Which number system has a base of 16 :		Octal		Hexadecimal	Hexadecimal Hexadecimal numbe
. Counting in hex, each digit can be increment from:	0 to F	0 to G		0 to J	0 to F
Which number can be converted into binary numbers by converted each he			Octal number		Hexadecimal numbe
. How many system of arithmetic, which are often used in digital system		6	3		4
Which are the system of arithmetic, which are often used in digital system		Decimal digit	Hexadecimal digit	All of these	All of these
A number system that uses only two digits, 0 and 1 is called the	Octal number system	Binary number syste	Decimal number sy:	Hexadecimal number	Decimal number sys
Which of the following gates is known as coincidence detector?	AND	OR	NOT	NAND	AND
An inverter is also called as	NOT	OR			NOT
Which gate has two or more input signals in which all input must be high t		NAND		NOR	AND
A NOR gate has a high output only when the input bits are	low	high	-	None of the above	
A NOR gate is logically equivalent to an OR gate followed by an	AND				INVERTER
Boolean expression for NOR gate with two inputs x and y can be written a Boolean expression for NAND gate with two inputs x and y can be written			•	xy' + x'y None of the above	(x+y)'
Boolean expression for NAND gate with two inputs x and y can be written NAND gates can be used as which type gates?	xy NOT	x+y OR	x'+y' AND	None of the above All of the above	X+y All of the above
An OR gate can be imagined as					switches connected
Which gate is known as Universal gate?	NOT		NAND		NAND
Any Boolean expression can be implemented using	only NOR gates		only AND gates		only NAND gates
Which digits are used to represent high & low level in digital circuits?	10	01	00		10
Complement of a Variable is represented by over the Letter.	slash	bar	dot	hyphen	bar
What is the value for 1+1 in Boolean Addition?	2	10			1
Multiplication in Boolean algebra is the same as the function				NOR	AND
The operation of an inverter is		-	-	-	Complement Input v
is same as Inversion.	complementation	exclusive	AND		complementation
The output of an AND gate is 1(High) only when both inputs are The output of an OR gate is 1(High) only when any one or more of the inp	10 bigb	0 0 low	1 1 medium		1 1 high
NAND is a complement of	NOT	AND		•	AND
NOR is a complement of	NOT	AND			
					OR A+B=B+A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as	A+B=B+A AB=BA	B+A=B+A BA=BA	A+B=A+B	AB=AB	A+B=B+A AB=BA
Commutative Law of Addition of 2 variables is written as	A+B=B+A AB=BA	B+A=B+A BA=BA	A+B=A+B	AB=AB AB=A+B	A+B=B+A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as	A+B=B+A AB=BA	B+A=B+A BA=BA	A+B=A + B AB=AB	AB=AB AB=A+B AB+C = A+BC	A+B=B+A AB=BA
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A+(B+C)=(A{+}B)\end{array}$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB	A+B=A+B $AB=AB$ $(A+B) C = A (B+C)$ $AB = BA$ $AB + C= AC + B$	$\begin{array}{l} AB{=}AB\\ AB{=}A{+}B\\ AB{+}C = A{+}BC\\ ABC{=}CAB\\ (AB) + C = AB + A\end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 = ?$	A+B=B+A $AB=BA$ $A+(B+C) = (A+H)$ $A (BC)=(AB) C$ $A (B+C) = AB + AC$ $A$	B+A=B+A BA=BA (A + B)+C = (A+B) ABC=ACB AB + AC = ABC 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A \end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1 \end{array}$	A+B=B+A $AB=BA$ $A+(B+C) = (A+B)$ $A (BC)=(AB) C$ $A (B+C) = AB + A(C)$ $A$
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}\left(B{+}C\right){=}\left(A{+}I\\ A\left(BC\right){=}\left(AB\right)C\\ A\left(B{+}C\right){=}AB{+}A\\ A\\ A\end{array}$	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B\\ ABC=ACB\\ AB+AC=ABC\\ 0\\ 0\\ \end{array}$	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B) A (BC)=(AB) C A (B+C) = AB + A(C) A 1
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A \cdot 0 =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C)=(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C)=AB{+}AC\\ A\\ A\\ A\\ A\end{array}$	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B)\\ ABC=ACB\\ AB+AC=ABC\\ 0\\ 0\\ 0\\ \end{array}$	$\begin{array}{l} A{+}B{=}A{+}B\\ AB{=}AB\\ (A{+}B)C{=}A(B{+}C)\\ AB{=}BA\\ AB{+}C{=}AC{+}B\\ -A\\ -A\\ -A\\ -A\end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A . 0 =$ ? In Boolean algebra $A . 1 =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C)=(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C)=AB{+}AA\\ A\\ A\\ A\\ A\\ A\end{array}$	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B)\\ ABC=ACB\\ AB+AC=ABC\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ \end{array}$	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B) C=A (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A . 0 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A . 1 =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}AA\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A\end{array}$	B+A=B+A BA=BA (A + B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A . 0 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}AA\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A\end{array}$	B+A=B+A BA=BA (A + B)+ C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\end{array}$	AB=AB AB=A+B AB+C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A 1
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A . 0 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}AA\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A\end{array}$	B+A=B+A BA=BA (A + B)+ C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\end{array}$	AB=AB AB=A+B AB+C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 1	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A . 0 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A . 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}AA\\ A\\ A\end{array}$	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B\\ ABC=ACB\\ AB-AC=ABC\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\$	A+B=A+B AB=AB (A+B) C = A (B+C) AB = BA AB + C = AC + B -A -A -A -A -A -A -A -A -A -A -A	AB=AB AB=A+B AB+C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 1 1 1 1 1	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (BC) = AB + A( A 1 0 A A A 1 A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}Aa\\ A(B{+}C){=}AB{+}Aa\\ A\\ A\\$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A{+}B{=}A{+}B\\ AB{=}AB\\ (A{+}B) C = A (B{+}C)\\ AB = BA\\ AB + C = AC + B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A$	AB=AB AB=A+B AB+C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A A 1 A A 0 0
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + A =$ ?	$\begin{array}{l} A{+}B{=}B{+}A\\ AB{=}BA\\ A{+}(B{+}C){=}(A{+}I\\ A(BC){=}(AB)C\\ A(B{+}C){=}AB{+}AA\\ A\\ $	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B)\\ ABC=ACB\\ AB+AC=ABC\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\$	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)\ C=A\ (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ \end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB=C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A A 1 A A 0 A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A \cdot 1 =$ ? In Boolean algebra $A \cdot 1 =$ ? In Boolean algebra $A \cdot 1 =$ ? In Boolean algebra $A \cdot A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ? In Boolean algebra $A + \overline{A} =$ ?	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AA\\ A\\ $	B+A=B+A BA=BA (A + B)+ C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B) C=A (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ B\\ Complement of the \end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB+C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A 1 A 0 A A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ?	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AA\\ A\\ $	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B\\ ABC=ACB\\ AB-AC=ABC\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\$	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B)C=A(B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ B\\ Complement of the two or more AND f \end{array}$	$\begin{array}{l} AB = AB \\ AB = A + B \\ AB = C = A + BC \\ ABC = CAB \\ (AB) + C = AB + A \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\$	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A=BA\\ A (BC)=(AB) C\\ A (BC)=(AB) C\\ A (B+C)=AB+AC\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A\\ A+B\\ A+BC\\ Sum of the Complet\\ two or more AND ft \end{array}$
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A \cdot A =$ ? In Boolean algebra $A \cdot A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A = $ ? In Boolean algebra $A =$ ? In Boolean algebra $A = $ ? In Boolean algebra $A =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A = $ ? In Boolean algebra $A =$ ? In Boolean Algebra $A = $ ? In Boolean Al	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AA\\ A\\ $	$\begin{array}{l} B+A=B+A\\ BA=BA\\ (A+B)+C=(A+B\\ ABC=ACB\\ ABC=ACB\\ AB+AC=ABC\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\ 0\\$	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B) C=A (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ Complement of the two or more AND f AND of two or more and the two or more and the two or more and the two or more and two$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB=C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\$	A+B=B+A AB=BA A (B)=(AB) C A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A 1 A A 1 A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A \cdot 0 =$ ? In Boolean algebra $A \cdot 1 =$ ? In Boolean algebra $A \cdot A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A =$ ? In Boolean Alg	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AC\\ A\\ A\\$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B) C=A (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ Complement of the two or more AND f\\ AND of two or more Distributive law \end{array}$	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB=C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ A+B\\ A+BC\\ Sum\\ two or more OR fur\\ OR of two or more\\ None of the above \end{array}$	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A A 1 A A 1 A A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A \cdot \overline{A} =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A + \overline$	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+B)C\\ A(BC)=(AB)C\\ A(B+C)=AB+AC\\ A\\ A\\$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic	AB=AB $AB=A+B$ $AB+C = A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $4 + B$ $A + BC$ Sum two or more OR fur OR of two or more None of the above NOR logic	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+B)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AC\\ A\\ B+C)=AB+AC\\ A\\ B+C\\ A\\ A\\$
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In B	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AC\\ A\\ A\\$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	$\begin{array}{l} A+B=A+B\\ AB=AB\\ (A+B) C=A (B+C)\\ AB=BA\\ AB+C=AC+B\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ -A\\ B\\ B\\ Complement of the two or more AND f AND of two or more AND f AND of two or more Distributive law NAND logic Complement State \\ \end{array}$	AB=AB $AB=A+B$ $AB+C = A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $1$ $1$	A+B=B+A AB=BA A+(B+C) = (A+B) A(BC)=(AB) C A(B+C) = AB + A(A) A 1 0 A A 1 0 A A 1 0 A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $(A + B) =$ ? The Complement of a Sum = Sum of Product expression is Boolean expression can be simplified using Sum of products expression is implemented with The output of an exclusive – OR gate is HIGH when inputs have The output of an exclusive – NOR gate is HIGH when inputs have	$\begin{array}{l} A+B=B+A\\ AB=BA\\ A+(B+C)=(A+I)\\ A(BC)=(AB)\\ C\\ A(B+C)=AB+AC\\ A\\ A\\$	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A B B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State Complement State	$\begin{array}{l} AB=AB\\ AB=A+B\\ AB=C=A+BC\\ ABC=CAB\\ (AB)+C=AB+A\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\$	$\begin{array}{l} A+B=B+A\\ AB=BA\\ AB=BA\\ A(BC)=(AB)C\\ A(BC)=(AB)C\\ A(B+C)=AB+AC\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ A\\ A\\ A\\ 1\\ A\\ A\\ A\\ B\\ A\\ A+B\\ A\\ A+B\\ A\\ A+BC\\ Sum of the Complet two or morel AND fit AND of two or morel and laws of Bc AND-OR logic Opposite State Same state\\ Same state \end{array}$
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of addition is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In B	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A S A A A S A A A S A A A S A	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or more Distributive law NAND logic Complement State Complement State X-OR / OR	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	A+B=B+A AB=BA A+(B+C) = (A+B) A(BC)=(AB) C A(B+C) = AB + A(A) A 1 0 A A 1 0 A A 1 0 A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $(A + B) =$ ? In Boolean algeb	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mot AND of two or	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or more Distributive law NAND logic Complement State Complement State X-OR / OR	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	A+B=B+A AB=BA A (B - C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A 1 A A 1 A A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolea	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mot AND of two or	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A -A -A	AB=AB AB=A+B AB=C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 4 + B A +BC Sum two or more OR fur OR of two or more None of the above NOR logic Alternate State Alternate State NAND / AND open input or outpu Junction Exclusive – NAND	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A A 1 1 A A 0 A A A 1 1 A A 0 A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A - 0 =$ ? In Boolean algebra $A - 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $(A + B) =$ ? In Boolean algebra $(A + B) =$ ? The Complement of a Sum = Sum of Product expression is Boolean expression is implemented with The output of an exclusive – OR gate is HIGH when inputs have The output of an exclusive – NOR gate is HIGH when inputs have Any Logic Expression can be implemented using What are the common internal gate failures? The interconnecting paths represent a common electrical point is known as The coincidence circuit is otherwise called as NAND & NOR gates are called as	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + AC A A A A A A A A A A A A A A A A A A	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State Complement State Complement State Complement State Complement State Complement State Complement State Complement State Complement State Complement or output Point Exclusive – AND Logical Gates	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	$\begin{array}{l} A+B=B+A\\ AB=BA\\ AB=BA\\ A=BA\\ A=(BC)=(AB)C\\ A\\ (BC)=(AB)C\\ A\\ (B+C)=AB+AC\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ 0\\ A\\ A\\ 1\\ A\\ A\\ A\\ 1\\ A\\ A\\ A\\ A\\ B\\ A\\ A+B\\ A\\ A+B\\ A\\ A+B\\ A\\ A+B\\ C\\ Sum of the Complettwo or more AND fiAND of two or morules and laws of BCAND-OR logicOpposite StateSame stateNAND / NORopen input or output JunctionLinction CRUniversal Gates$
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A - \overline{A} =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A +$	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A A A A A Sum of the Comple two or more AND of AND of two or more AND of two o	B+A=B+A BA=BA (A+B)+C = (A+B) ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or more Distributive law NAND logic Complement State Complement State Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates demorgan's postulat	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	A+B=B+A AB=BA A = (B + C) = (A+B) A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A 1 0 A A 1 A A 1 A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as Distributive Law is stated as Distributive Law is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ?	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mor AND of two or mor ASSOCIATIVE Iaw AND-OR logic Same state NAND / NOR open input or output Cell Exclusive-NOR Universal Gates demorgan's theorem eight minterms	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	A+B=B+A AB=BA A (B)=(AB) C A (BC)=(AB) C A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A A 1 A A A A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A - A =$ ? In Boolean algebra $A + A =$ ? In Boolean alge	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mor AND or mor	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A Complement of the two or more AND f AND of two or more Distributive law NAND logic Complement State Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates demorgan's postulat five minterms AND gates	AB=AB $AB=A+B$ $AB=C=A+BC$ $ABC=CAB$ $(AB) + C = AB + A$ $I$	A+B=B+A AB=BA A (B)=(AB) C A (BC)=(AB) C A (BC)=(AB) C A (BC)=(AB) C A 1 0 A A A 1 A A 1 A A A A A A A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Distributive Law is stated as Distributive Law is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + \overline{A} $	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mor AND of tw	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates demorgan's postulat five minterms AND gates 0	AB=AB AB=A+B AB=A+B ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 3 4 + B A + B A + BC Sum two or more OR fur OR of two or more None of the above NOR logic Alternate State Alternate State Alternate State NAND / AND open input or outpu Junction Exclusive – NAND Combinational gate algebric postulate four minterms XOR gates 1	A+B=B+A AB=BA A (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + A( A 1 0 A A 1 A A 1 A A 1 A A A A A A B A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolea	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + AC A A A A A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mor AND of two or mor ASSOCIATIVE I aw AND-OR logic Same state Same state Sa	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A -A -A	AB=AB AB=A+B AB=C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 3 4 + B A + B A + BC Sum two or more OR fur OR of two or more None of the above NOR logic Alternate State Alternate State A	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A A 1 1 A A 1 A A 1 A A A A A A B A A A B A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of Multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + A =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolean algebra $A - \overline{A} =$ ? The Complement of a Sum = Sum of Product expression is Product of Sum expression is Boolean expression can be simplified using Sum of products expression is implemented with The output of an exclusive – OR gate is HIGH when inputs have Any Logic Expression can be implemented using What are the common internal gate failures? The interconnecting paths represent a common electrical point is known as The coincidence circuit is otherwise called as NAND & NOR gates are called as Sum of products can be done using Two variables will be represented by The output of AND gates in SOP is connected to The minterms in a karnaugh map are marked with a Small circle in a NAND circuit represents Tabulation method is adopted for giving simplified function in	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + At A A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or mor AND of tw	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates demorgan's postulat five minterms AND gates 0	AB=AB AB=A+B AB=A+B ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 3 4 + B A + B A + BC Sum two or more OR fur OR of two or more None of the above NOR logic Alternate State Alternate State Alternate State NAND / AND open input or outpu Junction Exclusive – NAND Combinational gate algebric postulate four minterms XOR gates 1	A+B=B+A AB=BA A + (B + C) = (A+B A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A A 1 1 A A 1 A A 1 A A A A A A B A A A B A A A A
Commutative Law of Addition of 2 variables is written as Commutative Law of Multiplication of 2 variables is written as Associative law of multiplication is stated as Associative law of multiplication is stated as Distributive Law is stated as In Boolean algebra $A + 0 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 1 =$ ? In Boolean algebra $A + 3 =$ ? In Boolean algebra $A + \overline{A} =$ ? In Boolea	A+B=B+A AB=BA A + (B + C) = (A+I A (BC)=(AB) C A (B+C) = AB + AC A A A A A A A A A A A A A Sum of the Comple two or more AND f AND of two or more AND of two or more AND of two or more AND of two or more ASSociative law AND-OR logic Same state Same state Same state NAND / NOR open input or output Cell Exclusive-NOR Universal Gates demorgan's theorem eight minterms NOT gates y input subtraction of sum	B+A=B+A BA=BA (A + B)+ C = (A+B ABC=ACB AB + AC = ABC 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	A+B=A + B AB=AB (A+B) C = A (B+C) AB = BA AB + C= AC + B -A -A -A -A -A -A -A -A B B Complement of the two or more AND f AND of two or mor Distributive law NAND logic Complement State Complement State Complement State Complement State X-OR / OR open input or outpu Point Exclusive – AND Logical Gates demorgan's postulat five minterms AND gates 0 output product of sums	AB=AB AB=A+B AB=C = A+BC ABC=CAB (AB) + C = AB + A 1 1 1 1 1 1 1 1 1 1 4 + B A + BC Sum two or more OR fur OR of two or more NOR logic Alternate State Alternate State Alternate State NAND / AND open input or outpu Junction Exclusive – NAND Combinational gate: algebric postulate four minterms XOR gates 1 complement subtraction of produ maxterm	A+B=B+A AB=BA A (B-C) = (A+B A (BC)=(AB) C A (B+C) = AB + AC A 1 0 A A 1 A A A 1 A A A A A A A A A A