About the Institution

Karpagam Academy of Higher Education genesis goes back to September 1995 when it was initiated as Karpagam Arts & Science College. The college was promoted by the Karpagam Charity Trust, incepted by the renowned Karpagam Industries. Today, more than 15 years later, those humble beginnings have paved the way for us to emerge as a University of repute, a status granted by the MHRD and UGC (Under section 3 of UGC Act, 1956) in 2008.

The passion and endeavour to ensure excellence in education is what we believe, that has been instrumental in our evolution. Our commitment to extend world-class education permeates across the multi-disciplines we offer - Arts, Science, Commerce, Management, Humanities and Engineering.

About the location

The KAHE is situated 10 kms from Coimbatore, on the junction of Coimbatore - Pollachi National Highway (NH - 209) & Coimbatore Bye - Pass road (NH 47), Tamil Nadu, INDIA.

About the Department

Communication systems are transforming the way the world functions today. Innovations in networking are bringing the world together like never before. The field of Electronics & Communication Engineering is at the forefront of exciting new possibilities. At the Faculty of Engineering, the Department of ECE is well geared to tune our students to the technological revolutions in this engineering discipline. The Departmental Labs are well equiped with recent advanced Devices and Softwares to enhance the Practical knowledge of the Student and that develop research skills in the above field.
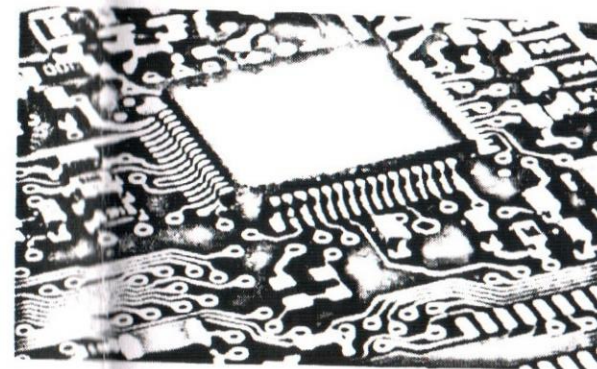
CADENCE EDA TOOLS

Chip design in India is poised to move into the big league with multinationals, design services companies, product companies. Start - ups in the country grow day by day. Senior semiconductor executives, trade bodies such as NASSCOM, see this as a sunrise industry. They find that the only impediment is the potential shortfall of quality VLSI - trained talent. Hence, this workshop strives to build the manpower pool, with the main aim of producing India a VLSI Design powerhouse in the global semiconductor design and development world. Cadence is committed in helping its customers by providing them with a pool of engine   d in EDA tool use and methodologies

KARPA   ACADEMY
HIGHER EDUCATION

WORKSHOP ON ANALOG & DIGITAL IC DESIGN USING CADENCE EDA TOOLS OCT 9 & 10 2015

Organized By

Department of
Electronics and Communication Engineering,
Faculty of Engineering
Karpagam Academy of Higher Education
Pollachi main road, Eachanari post
Coimbatore-641021
Tamilnadu.

Objective of the workshop

Workshop on "Analog & Digital IC Design using cadence EDA tools" focusses on basic IC Design flow

In this workshop RTL based digital logic design techniques, different layout analysis techniques, physical layout design of inverter and its physical verification are covered completely. This Workshop will be usefull to all budding VLSI Engineers.

Topics to be covered

Day 1:

-Digital Logic Design using VerilogHDL code & Testbench

-Finite State Machine (FSM) designs.

-Simulations using Cadence ncSim tool.

-Synthesizing to generate netlist using Cadence rcLab tools.

-Introduction to IPcore (Intellectual Property).

Day 2:

-Analog Design Flow

-Introduction to cadence Virtuoso Tools

-Transient Response and DC analysis

-Power and delay calculation

-Creating layout of Inverter

-DRC, LVS & RC extraction using Assura

-GDS Translation

Important dates

Last date for Payment	: 05-10-2015
Intimation of Confirmation	: 07-10-2015
Duration of programme	: 9th to10th OCT 2015

Registration & Payment mode

The Participants willing to join, must register their names by sending the filled Registration Form. Only 30 Participants are allowed on first come first serve basis.

Website : www.ectrix.webs.com

The Registration form in the prescribed format, along with DD drawn in favour of "KARPAGAM UNIVERSITY", payable at Coimbatore , to be sent to the below mentioned contact address on or before 5th Oct 2015.

Students - UG	: Rs. 700/-
Students - PG	: Rs. 800/-
Faculty & Research scholars	: Rs. 1000/-

Registration fee includes registration kit, Certificate and refreshment.

Organising committe

Convener

Dr. S. Bhavani,
HOD/ECE, FOE (CB), KAHE.

Coordinators

Mr.C.Pandiarajan   - +91 9994635218
AP/ECE, FOE(CB), KAHE.

Mr.G.R.Mahendrababu   - +91 9965539275
AP/ECE, FOE(CB), KAHE.

Address for Communication

Dr. S. Bhavani, HOD/ECE,
Faculty of Engineering (CB),
Karpagam Academy of Higher Education,
Pollachi main road,Eachanari Póst,
Coimbatore-641021.

WORKSHOP ON ANALOG & DIGITAL IC DESIGN USING CADENCE EDA TOOLS OCT 9th & 10th 2015

REGISTRATION

Name : ....

Designation : ....

Department : ....

Institution : ....

Organization ....

Address : ....

....

.............................PIN:

Phone/Mobile : ....

....

E-mail ID : ....

I have enclosed DD (in favour of "KARPAGAM UNIVERSITY") for the amount of Rs..........
towards registration fees.

DD No.:...............................

Date:..................................

Bank: ....

Date :

Place :   Signature of the Candida