REGISTRATION FORM

One Day Hands on Workshop
On
“ANALOG VLSI DESIGN USING CADENCE EDA TOOLS”
25th September, 2018
by
Department of Electronics & Communication Engineering
Karpagam Academy of Higher Education
Coimbatore-641 021, Tamil Nadu.

Name :
Designation :
Phone No :
Mail ID :
Amount :
Institution :
Address :

Signature of the participant

This is to certify that _______________ is regular faculty of our Institution and is hereby sponsored for attending one day “HANDS ON WORKSHOP ON ANALOG VLSI DESIGN USING CADENCE EDA TOOLS” at Karpagam Academy of Higher Education, Coimbatore on 25th September, 2018. He / She will be permitted to attend the workshop for the entire duration if selected.

Head of the Institution with seal

ORGANIZING COMMITTEE

Chief Patron
Dr. S. Sudalaimuthu
Vice Chancellor
Karpagam Academy of Higher Education

Patron
Dr. R. Sundararajan
Registrar
Karpagam Academy of Higher Education

Convenors
Dr. G. K. D. Prasanna Venkatesan
Dean
Faculty of Engineering
Karpagam Academy of Higher Education

Dr. S. Bhavani
Head of the Department
Department of Electronics and communication Engg
Faculty of Engineering
Karpagam Academy of Higher Education

Organizing Secretaries
Mr. G. R. Mahendra Babu, M.E -9965539275
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Department of Electronics & Communication Engg
Faculty of Engineering
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KARPAGAM ACADEMY OF HIGHER EDUCATION
(Deemed to be University)
Established Under Section 3 of UGC Act, 1956

Faculty of Engineering
One Day Hands on Workshop
On
“ANALOG VLSI DESIGN USING CADENCE EDA TOOLS”
On
25th September, 2018

Organized By
Department of Electronics & Communication Engg
Karpagam Academy of Higher Education
Coimbatore-641 021
Tamil Nadu, India
ABOUT THE INSTITUTION

Karpagam Academy of Higher Education (KAHE) established under Section 3 of UGC Act, 1956 is approved by Ministry of Human Resource and Development, Government of India. Dr. R. Vasanthakumar, the President of the Trust, a Philanthropist, Industrialist, Entrepreneur and Culture Promoter.

Contemporary infrastructure, modern teaching methodologies, career oriented training, excellent placements and the finest faculty have always been Karpagam's hallmark. Besides technical expertise, the Institution has made a mark for itself, since its inception by developing communication and soft skills, ensuring enlightening knowledge, extending holistic education and creating a strong value system.

ABOUT THE DEPARTMENT

Communication systems are transforming the way world functions today. Innovations in networking are bringing the world together like never before. The field of electronics and communication is at forefront of exciting new possibilities. In the faculty of Engineering, the department of ECE geared to tune our students to the technology revolution in engg., discipline. Advanced devices and software’s are available to enhance the practical knowledge of the student and to improve the research skills in the above field.

CADENCE EDA TOOLS

Chip design in India is poised to move into the big step with multinational, design service industries, product companies, startups in the country grow day by day.

Senior semiconductor executives, Trade bodies such as NASSCOM see this as sunrises Industry and state that the only impediment is potential shortfall of the quality VLSI trained talent. Hence this workshop strives to build the manpower pool, with the main aim of producing India a VLSI Design power house in the global semiconductor design and development world. Cadence is committed to helping its customers by providing them with a pool of engineers experienced in EDA tools usage and methodologies.

OBJECTIVE OF THE WORKSHOP

One day national level “HANDS ON WORKSHOP ON ANALOG VLSI DESIGN USING CADENCE EDA TOOLS” focuses on the concept of the Analog circuit design and will be useful to faculty, students and research scholars interested in analog circuit design. The full day session is hands on based using virtuoso tool.

TOPICS TO BE COVERED

- Analog Design Flow
- Introduction to Cadence virtuoso tool
- Transient Response and DC analysis
- Creating layout of Inverter
- DRC ,LVS ,RC extraction using Assura
- GDS Translation.

REGISTRATION DETAILS

- Student (UG&PG): Rs.300
- Faculty /Research scholars: Rs.400
- Industry participation :Rs.500

Please send the scanned copy of filled in registration form along with DD drawn in favor of Karpagam Academy of Higher Education payable at Coimbatore on or before 22-09-2018. Spot registration is subjected to availability of seats.

Intimation of acceptance by mail is on or before 22nd September 2018.

Accommodation will be provided only based on request and payment basis. The Registration fee includes working lunch and refreshment.

IMPORTANT DATES

- Last date of payment :22.09.18
- Intimation of confirmation:24.09.18
- Date of program:25.09.18

ADDRESS FOR COMMUNICATION

Dr.Bhavani, HOD/ECE,
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Eachnari post,
Coimbatore-641021,
Tamilnadu,INDIA.